

Fast Recovery Epitaxial Diode (FRED)

preliminary data

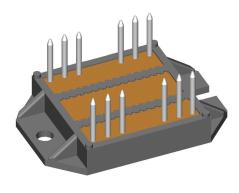
 $I_{FAVM} = 2x 165 A$

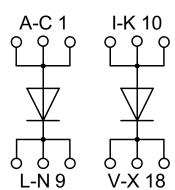
 $V_{RRM} = 200 V$

 $t_{rr} = 35 \, \text{ns}$

Part number

DSEI2x161-02P





Features / Advantages:

- 2 independent FRED in 1 package
- · Planar passivated chips
- · Very short recovery time
- · Leads suitable for PC board soldering
- Very short recovery time
- · Soft recovery behaviour
- · Easy to mount with two screws
- Space and weight savings
- Improved temperature and power cycling capability
- Low noise switching
- · Small and light weight

Applications:

- Antiparallel diode for high frequency switching devices
- · Anti saturation diode
- Snubber diode
- Free wheeling diode in converters and motor control circuits
- Rectifiers in switch mode power supplies (SMPS)
- · Inductive heating and melting
- Uninterruptible power supplies (UPS)
- Ultrasonic cleaners and welders

Package: ECO-PAC2

- Isolation voltage: 3000 V~
- · Industry standard outline
- · RoHS compliant
- Soldering pins for PCB mounting
- Height: 9 mm
- Base plate: DCB ceramic
- · Reduced weight
- Advanced power cycling

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Diode					Ratings			
Symbol	Definitions	Conditions		min.	typ.	max.		
FRMS	RMS forward current		$T_{VJ} = T_{VJM}$			270	Α	
I _{FAVM} ①	max. average forward current	rectangular, d = 0.5	$T_{\rm C} = 70^{\circ} C$			165	A	
FSM	max. surge forward current	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	$T_{VJ} = 45^{\circ}C$			1200 1300	A A	
		t = 10 ms (50 Hz), sine $t = 8.3 ms$ (60 Hz), sine	$T_{VJ} = 150^{\circ}C$			1080 1170	A A	
l²t	Pt value for fusing	t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine	$T_{VJ} = 45^{\circ}C$			7200 7100	A ² s A ² s	
		t = 10 ms (50 Hz), sine $t = 8.3 ms$ (60 Hz), sine	$T_{VJ} = 150^{\circ}C$			5800 5700	A²s A²s	
I _R	reverse current	$V_R = V_{RRM}$ $V_R = 0.8 \cdot V_{RRM}$ $V_R = 0.8 \cdot V_{RRM}$	$T_{VJ} = 25^{\circ}C$ $T_{VJ} = 25^{\circ}C$ $T_{VJ} = 125^{\circ}C$			3 2 80	mA mA mA	
V _F	forward voltage	I _F = 200 A	$T_{VJ} = 25^{\circ}C$			1.2	V	
ν _{το}	threshold voltage slope resistance	for power-loss calculations only $T_{VJ} = T_{VJIN}$				0.53 2.6	V mΩ	
R _{thJC}	thermal resistance junction to case thermal resistance junction to heatsink				0.20	0.29	K/W K/W	
I _{RM}	max. reverse recovery current	$I_F = 100 \text{ A}; -di_F/dt = 200 \text{ A/}\mu\text{s}$ $T_{VJ} = 1$ $V_R = 100 \text{ V}; L \le 0.05 \mu\text{H}$			20		A	
t _{rr}	reverse recovery time	I _F = 1 A; -di/dt = 400 A/µs; V _R = 30 V	T _{vJ} = 25°C		35		ns	

① I_{FAVM} rating includes reverse blocking losses at T_{VJM} , V_{R} = 0.8 V_{RRM} , duty cycle d = 0.5





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Package	ECO-PAC2			Ratings			
Symbol	Definitions	Conditions		min.	typ.	max.	
I _{RMS}	RMS current per terminal				100	A	
T _{vJ} T _{op} T _{stg}	virtual junction temperature operation temperature storage temperature		-40 -40 -40		150 125 125	0° 0° 0°	
Weight					24		g
M _D	mounting torque			1.4		2.0	Nm
d _{Spp/App}	creepage distance on surface I striking distance through air terminal to terminal to backside		6.0 10.0			mm mm	
V _{ISOL}	isolation voltage	t = 1 second $t = 1 \text{ minute}$ 50/60 Hz, RMS; $I_{ISOL} \le 1 \text{ mA}$		3000 2500			V

