

Date: - 12th May 2004

Data Sheet Issue:- 1

# Soft Recovery Diode Type M0358WC120 to M0358WC180

Old Type No.: SM12-18CXC100

#### **Absolute Maximum Ratings**

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
$V_{RRM}$	Repetitive peak reverse voltage, (note 1)	1200-1800	٧
$V_{RSM}$	Non-repetitive peak reverse voltage, (note 1)	1300-1900	V

	OTHER RATINGS	MAXIMUM LIMITS	UNITS
$I_{F(AV)M}$	Maximum average forward current, T <sub>sink</sub> =55°C, (note 2)	358	Α
$I_{F(AV)M}$	Maximum average forward current. T <sub>sink</sub> =100°C, (note 2)	157	Α
$I_{F(AV)M}$	Maximum average forward. T <sub>sink</sub> =100°C, (note 3)	85	Α
I <sub>F(RMS)</sub>	Nominal RMS forward current, T <sub>sink</sub> =25°C, (note 2)	732	Α
I <sub>F(d.c.)</sub>	D.C. forward current, T <sub>sink</sub> =25°C, (note 4)	578	Α
I <sub>FSM</sub>	Peak non-repetitive surge t <sub>p</sub> =10ms, V <sub>RM</sub> =60%V <sub>RRM</sub> , (note 5)	2450	Α
I <sub>FSM2</sub>	Peak non-repetitive surge t <sub>p</sub> =10ms, V <sub>RM</sub> ≤10V, (note 5)	2695	Α
l <sup>2</sup> t	I <sup>2</sup> t capacity for fusing t <sub>p</sub> =10ms, V <sub>RM</sub> =60%V <sub>RRM</sub> , (note 5)	30.0×10 <sup>3</sup>	A <sup>2</sup> s
l <sup>2</sup> t	I²t capacity for fusing t <sub>p</sub> =10ms, V <sub>RM</sub> ≤10V, (note 5)	36.3×10 <sup>3</sup>	A <sup>2</sup> s
T <sub>j op</sub>	Operating temperature range	-40 to +125	°C
$T_{stg}$	Storage temperature range	-40 to +125	°C

#### Notes:-

- 1) De-rating factor of 0.13% per °C is applicable for T<sub>i</sub> below 25°C.
- 2) Double side cooled, single phase; 50Hz, 180° half-sinewave.
- 3) Single side cooled, single phase; 50Hz, 180° half-sinewave.
- 4) Double side cooled.
- 5) Half-sinewave,  $125^{\circ}C$  T<sub>j</sub> initial.



## **Characteristics**

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS (Note 1)	UNITS	
\/	Maximum pools forward voltage	-	-	1.97	I <sub>FM</sub> =635A	V	
$V_{FM}$	Maximum peak forward voltage	-	-	2.1	I <sub>FM</sub> =750A	V	
$V_{T0}$	Threshold voltage	-	-	1.46		V	
$r_{T}$	Slope resistance	-	-	0.80		mΩ	
N/ Mariana famoral as a 11		-	-	35	di/dt = 1000A/μs, 25°C	V	
$V_{FRM}$	Maximum forward recovery voltage	-	- 80 di/dt = 1000A/µs		di/dt = 1000A/µs	V	
I <sub>RRM</sub>	Peak reverse current	-	-	20	Rated V <sub>RRM</sub>	mA	
$Q_{rr}$	Recovered charge	-	125	-		μC	
$Q_{ra}$	Recovered charge, 50% Chord	-	65	90	  I <sub>FM</sub> =1000A, t <sub>p</sub> =500μs, di/dt=100A/μs,	μC	
I <sub>rm</sub>	Reverse recovery current	-	80	-	V <sub>r</sub> =50V, 50% Chord.	Α	
t <sub>rr</sub>	Reverse recovery time, 50% Chord	-	1.4	-		μs	
R	Thermal resistance innetion to be stainly	-	-	0.09	Double side cooled	K/W	
$R_{thJK}$	hermal resistance, junction to heatsink	-	-	0.18	Single side cooled	K/VV	
F	Mounting force	3.3	-	5.5		kN	
W <sub>t</sub>	Weight	-	70	-		g	

#### Notes:-

1) Unless otherwise indicated  $T_j$ =125°C.



#### **Notes on Ratings and Characteristics**

#### 1.0 Voltage Grade Table

Voltage Grade	$V_{RRM}$	V <sub>RSM</sub>	V <sub>R</sub> dc
	(V)	(V)	(V)
12	1200	1300	820
14	1400	1500	930
16	1600	1700	1040
18	1800	1900	1150

### 2.0 De-rating Factor

A blocking voltage de-rating factor of 0.13% per °C is applicable to this device for T<sub>i</sub> below 25°C.

#### 3.0 ABCD Constants

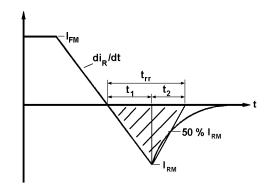
These constants (applicable only over current range of V<sub>F</sub> characteristic in Figure 1) are the coefficients of the expression for the forward characteristic given below:

$$V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}$$

where  $I_F$  = instantaneous forward current.

#### 4.0 Reverse recovery ratings

(i) Qra is based on 50% Irm chord as shown in Fig.(a) below.



(ii) Q<sub>rr</sub> is based on a 150μs integration time.

l.e. 
$$Q_{rr}=\int\limits_{0}^{150\,\mu s}i_{rr}.dt$$
 (iii)  $K\ Factor=rac{t_{1}}{t_{2}}$ 

(iii) 
$$K Factor = \frac{t_1}{t_2}$$

#### 5.0 Reverse Recovery Loss

The following procedure is recommended for use where it is necessary to include reverse recovery loss.

From waveforms of recovery current obtained from a high frequency shunt (see Note 1) and reverse voltage present during recovery, an instantaneous reverse recovery loss waveform must be constructed. Let the area under this waveform be E joules per pulse. A new sink temperature can then be evaluated from:

$$T_{SINK} = T_{J(MAX)} - E \cdot [k + f \cdot R_{thJK}]$$

Where  $k = 0.2314 \, (^{\circ}C/W)/s$ 

E = Area under reverse loss waveform per pulse in joules (W.s.)

*f* = Rated frequency in Hz at the original sink temperature.

 $R_{thJK}$  = d.c. thermal resistance (°C/W)

The total dissipation is now given by:

$$W_{(tot)} = W_{(original)} + E \cdot f$$

NOTE 1 - Reverse Recovery Loss by Measurement

This device has a low reverse recovered charge and peak reverse recovery current. When measuring the charge, care must be taken to ensure that:

- (a) AC coupled devices such as current transformers are not affected by prior passage of high amplitude forward current.
- (b) A suitable, polarised, clipping circuit must be connected to the input of the measuring oscilloscope to avoid overloading the internal amplifiers by the relatively high amplitude forward current signal.
- (c) Measurement of reverse recovery waveform should be carried out with an appropriate critically damped snubber, connected across diode anode to cathode. The formula used for the calculation of this snubber is shown below:

$$R^2 = 4 \cdot \frac{V_r}{C_S \cdot \frac{di}{dt}}$$

Where:  $V_r$  = Commutating source voltage

C<sub>S</sub> = Snubber capacitance R = Snubber resistance

#### 6.0 Snubber Components

When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

#### 7.0 Computer Modelling Parameters

#### 7.1 Device Dissipation Calculations

$$I_{AV} = \frac{-V_{T0} + \sqrt{V_{T0} + 4 \cdot ff^2 \cdot r_T \cdot W_{AV}}}{2 \cdot ff^2 \cdot r_T}$$

Where  $V_{T0}$  =1.46V,  $r_T$  = 0.8m $\Omega$ 

ff = form factor (normally unity for fast diode applications)

$$W_{AV} = \frac{\Delta T}{R_{th}}$$

$$\Delta T = T_{j(MAX)} - T_K$$

#### 7.2 Calculation of V<sub>F</sub> using ABCD Coefficients

The forward characteristic I<sub>F</sub> Vs V<sub>F</sub>, on page 6 is represented in two ways;

- (i) the well established  $V_{T0}$  and  $r_T$  tangent used for rating purposes and
- (ii) a set of constants A, B, C, and D forming the coefficients of the representative equation for V<sub>F</sub> in terms of I<sub>F</sub> given below:

$$V_F = A + B \cdot \ln(I_F) + C \cdot I_F + D \cdot \sqrt{I_F}$$

The constants, derived by curve fitting software, are given in this report for hot characteristics. The resulting values for  $V_F$  agree with the true device characteristic over a current range, which is limited to that plotted.

	25°C Coefficients	125°C Coefficients
Α	0.04024108	-0.873541163
В	0.3593347	0.5784799
С	6.9923×10 <sup>-4</sup>	9.91536×10 <sup>-4</sup>
D	-0.03048276	-0.0590576

#### 8.0 Frequency Ratings

The curves illustrated in figures 8 to 16 are for guidance only and are superseded by the maximum ratings shown on page 1.

#### 9.0 Square wave ratings

These ratings are given for load component rate of rise of forward current of 100 and 500 A/µs.

#### 10.0 Duty cycle lines

The 100% duty cycle is represented on all the ratings by a straight line. Other duties can be included as parallel to the first.

#### **Curves**

Figure 1 – Forward characteristics of Limit device

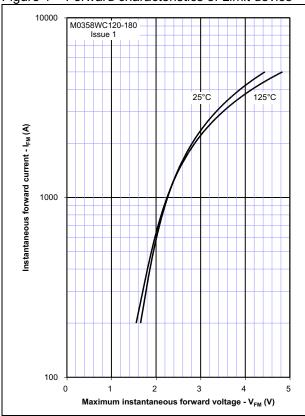


Figure 2 – Maximum forward recovery voltage

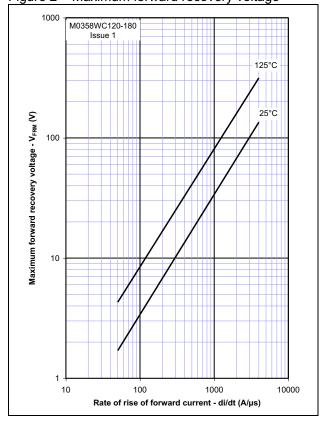


Figure 3 - Recovered charge, Q<sub>rr</sub>

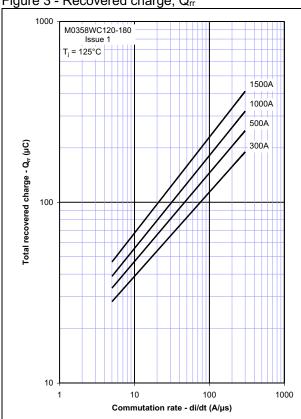


Figure 4 - Recovered charge, Qra (50% chord)

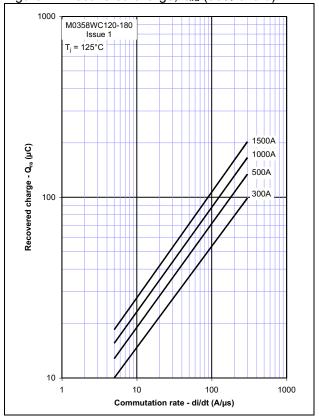


Figure 5 - Maximum reverse current, Irm

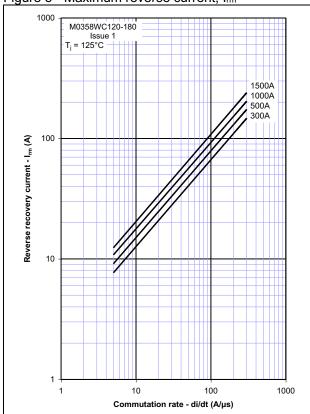


Figure 6 - Maximum recovery time, t<sub>rr</sub> (50% chord)

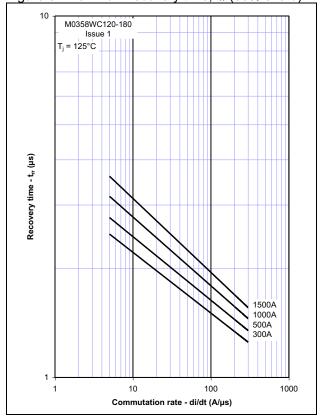


Figure 7 – Reverse recovery energy per pulse

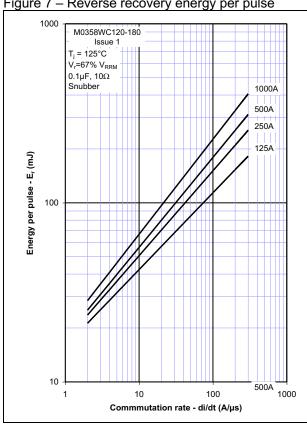


Figure 8 - Sine wave energy per pulse

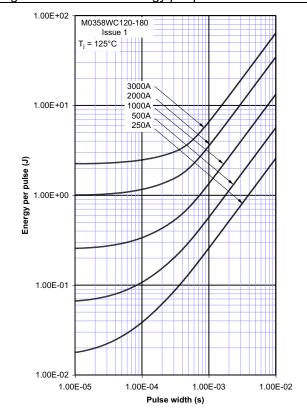


Figure 9 - Sine wave frequency vs. pulse width

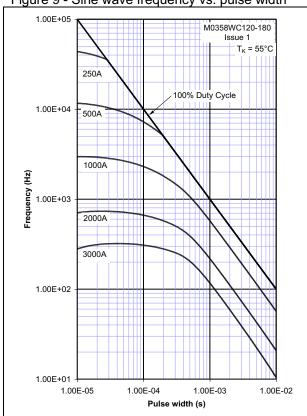


Figure 10 - Sine wave frequency vs. pulse width

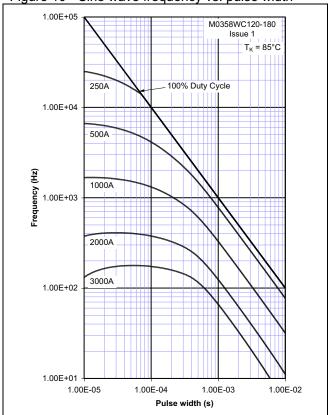


Figure 11 - Square wave energy per pulse

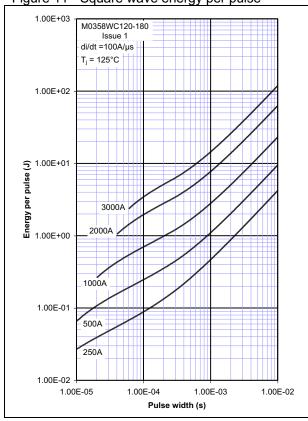


Figure 12 - Square wave energy per pulse

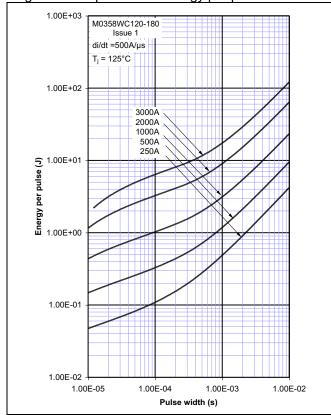


Figure 13 - Square wave frequency vs. pulse width

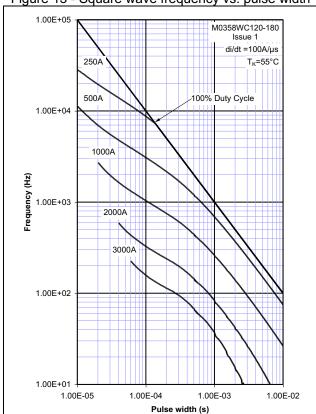


Figure 14 - Square wave frequency vs. pulse width

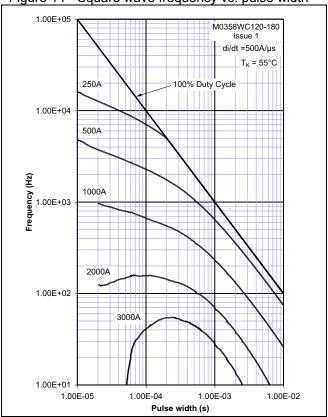


Figure 15 - Square wave frequency vs. pulse width

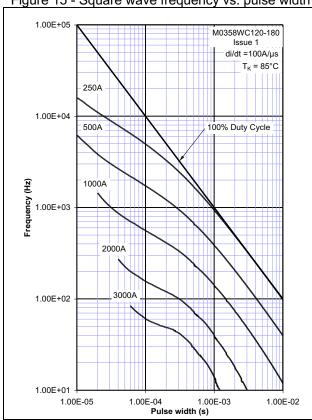
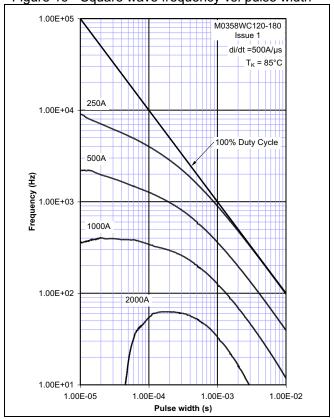
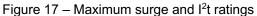


Figure 16 - Square wave frequency vs. pulse width





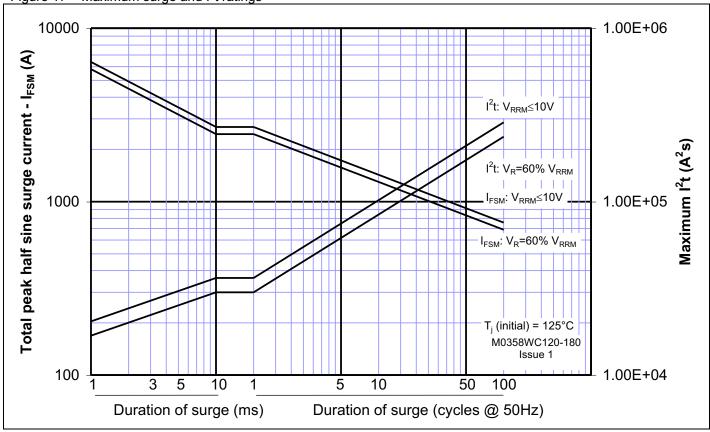
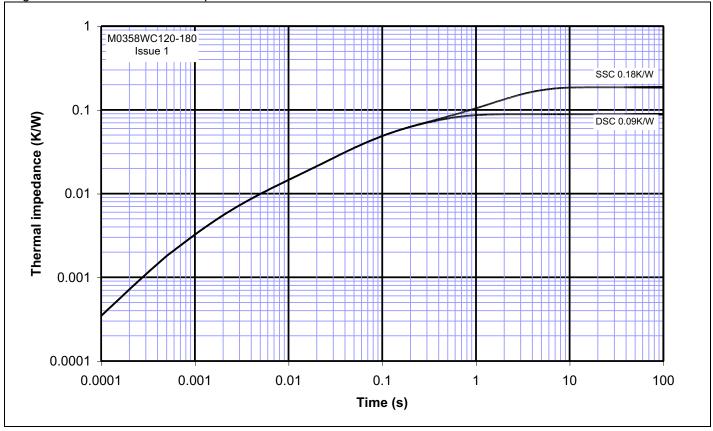
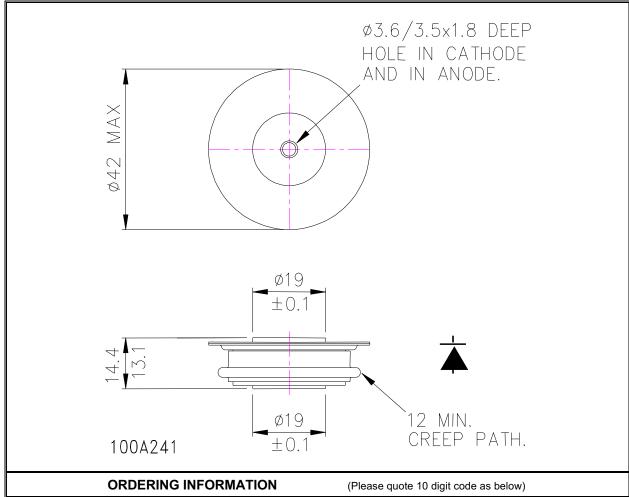


Figure 18 – Transient thermal impedance



#### **Outline Drawing & Ordering Information**



M0358	wc	<b>*</b>	0
Fixed Type Code	Fixed outline code	Voltage code V <sub>RRM</sub> /100 12-18	Fixed code

Order code: M0358WC160 - 1600V V<sub>RRM</sub>, 14.4mm clamp height capsule.

#### IXYS Semiconductor GmbH

Edisonstraße 15 D-68623 Lampertheim Tel: +49 6206 503-0

Fax: +49 6206 503-627 E-mail: marcom@ixys.de

# An IXYS Company

www.westcode.com

www.ixys.com

# E-mail: WSL.sales@westcode.com

Westcode Semiconductors Ltd

Langley Park Way, Langley Park, Chippenham, Wiltshire SN15 1GE.

Tel: +44 (0)1249 444524

Fax: +44 (0)1249 659448

Westcode Semiconductors Inc 3270 Cherry Avenue Long Beach CA 90807 USA Tel: +1 (562) 595 6971 Fax: +1 (562) 595 8182

E-mail: WSI.sales@westcode.com

### **IXYS** Corporation

3540 Bassett Street Santa Clara CA 95054 USA Tel: +1 (408) 982 0700

Fax: +1 (408) 496 0670 E-mail: sales@ixys.net

The information contained herein is confidential and is protected by Copyright. The information may not be used or disclosed except with the written permission of and in the manner permitted by the proprietors Westcode Semiconductors Ltd

In the interest of product improvement, Westcode reserves the right to change specifications at any time without prior notice.

Devices with a suffix code (2-letter, 3-letter or letter/digit/letter combination) added to their generic code are not necessarily subject to the conditions and limits contained in this report.

© Westcode Semiconductors Ltd.

