

Tentative Data

Insulated Gate Bi-Polar Transistor

Type T1440VC17E

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{CES}	Collector – emitter voltage	1700	V
$V_{DC\ link}$	Permanent DC voltage for 100 FIT failure rate.	900	V
V_{GES}	Peak gate – emitter voltage	± 20	V

	RATINGS	MAXIMUM LIMITS	UNITS
I_C	Continuous DC collector current, IGBT	1440	A
I_{CRM}	Repetitive peak collector current, $t_p=1ms$, IGBT	2880	A
I_{ECO}	Maximum reverse emitter current, $t_p=100\mu s$, (note 2 & 3)	1440	A
P_{MAX}	Maximum power dissipation, IGBT (note 2)	4.4	kW
$T_{j\ op}$	Operating temperature range	-40 to +125	$^{\circ}C$
T_{stg}	Storage temperature range	-40 to +125	$^{\circ}C$

Notes: -

- 1) Unless otherwise indicated $T_j = 125^{\circ}C$.
- 2) $T_{sink} = 25^{\circ}C$, double side cooled.
- 3) Maximum commutation loop inductance 110nH.

Characteristics

IGBT Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
$V_{CE(sat)}$	Collector – emitter saturation voltage	-	2.36	2.65	$I_C = 1440A, V_{GE} = 15V, T_J = 25^\circ C$	V
		-	3.0	3.3	$I_C = 1440A, V_{GE} = 15V$	V
V_{T0}	Threshold voltage	-	-	1.218	Current range: 480A –1440A	V
r_T	Slope resistance	-	-	1.445		mΩ
$V_{GE(TH)}$	Gate threshold voltage	-	5	-	$V_{CE} = V_{GE}, I_C = 48mA$	V
I_{CES}	Collector – emitter cut-off current	-	10	25	$V_{CE} = V_{CES}, V_{GE} = 0V$	mA
I_{GES}	Gate leakage current	-	-	±25	$V_{GE} = \pm 20V$	μA
C_{ies}	Input capacitance	-	113	-	$V_{CE} = 25V, V_{GE} = 0V, f = 1MHz$	nF
$t_{d(on)}$	Turn-on delay time	-	0.45	-	$I_C = 1440A, V_{CE} = 900V, di/dt = 8000A/\mu s$	μs
$t_r(V)$	Rise time	-	0.9	-		μs
$Q_{g(on)}$	Turn-on gate charge	-	6.5	-	$V_{GE} = \pm 15V, L_S = 110nH$	μC
E_{on}	Turn-on energy	-	0.7	-	$R_{G(ON)} = 1.2\Omega, R_{G(OFF)} = 10\Omega, C_{GE} = 180nF$	J
$t_{d(off)}$	Turn-off delay time	-	2.4	-	Freewheel diode type TBC at $T_J = 125^\circ C$ (Notes 3, 4 & 5)	μs
$t_f(I)$	Fall time	-	0.6	-		μs
$Q_{g(off)}$	Turn-off gate charge	-	3.6	-		μC
E_{off}	Turn-off energy	-	1.2	-		J
I_{sc}	Short circuit current	-	3600	-	$V_{GE} = +15V, V_{CC} = 900V, V_{CEmax} \leq V_{CES}, t_p \leq 10\mu s$	A

Thermal Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
R_{thJK}	Thermal resistance junction to sink, IGBT	-	-	22.5	Double side cooled	K/kW
		-	-	35.3	Collector side cooled	K/kW
		-	-	65.3	Emitter side cooled	K/kW
F	Mounting force	11	-	16	Note 2	kN
W_t	Weight	-	0.65	-		kg

Notes:-

- 1) Unless otherwise indicated $T_J = 125^\circ C$.
- 2) Consult application note 2008AN01 for detailed mounting requirements.
- 3) C_{GE} is additional gate - emitter capacitance added to output of gate drive circuit.
- 4) E_{on} integration time 15μs from 10% rising I_G .
- 5) E_{off} integration time 15μs from 90% falling V_{GE} .

Curves

Figure 1 – Typical collector-emitter saturation voltage characteristics

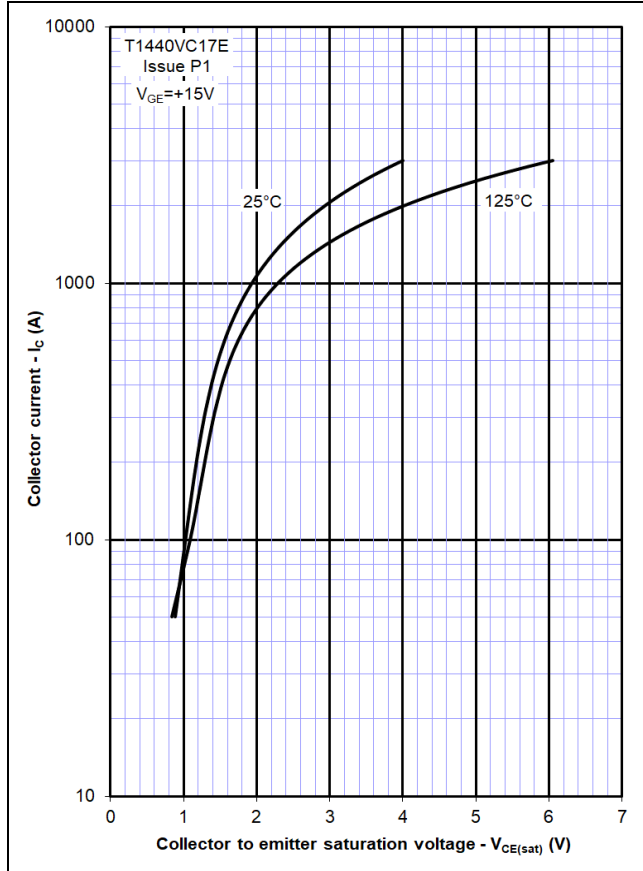


Figure 2 – Typical output characteristic

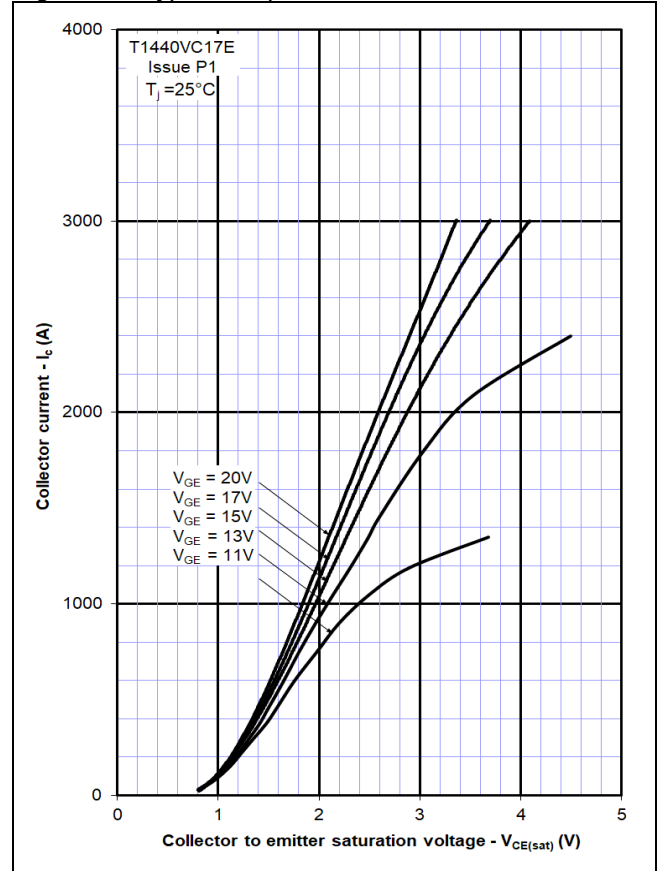


Figure 3 – Typical output characteristic

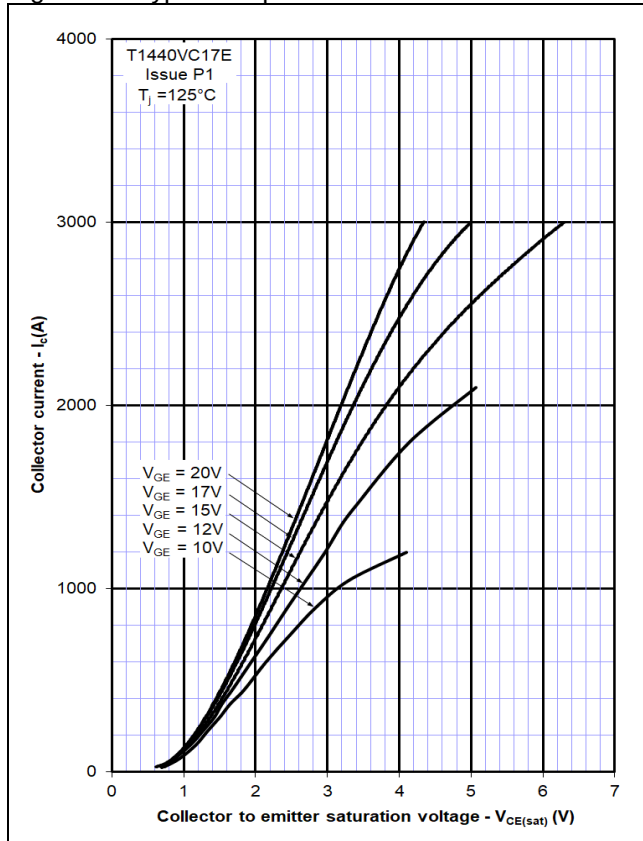


Figure 4 – Safe operating area

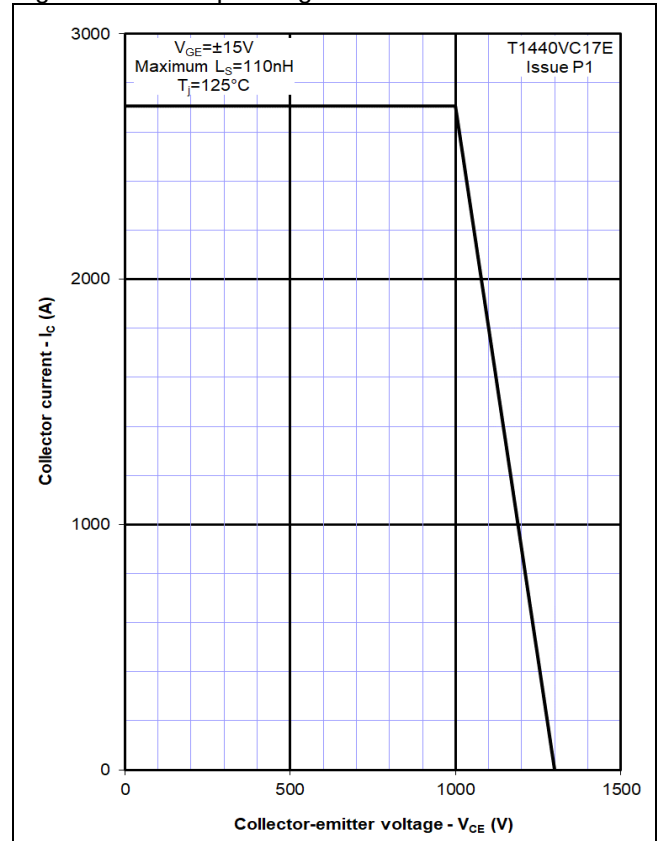
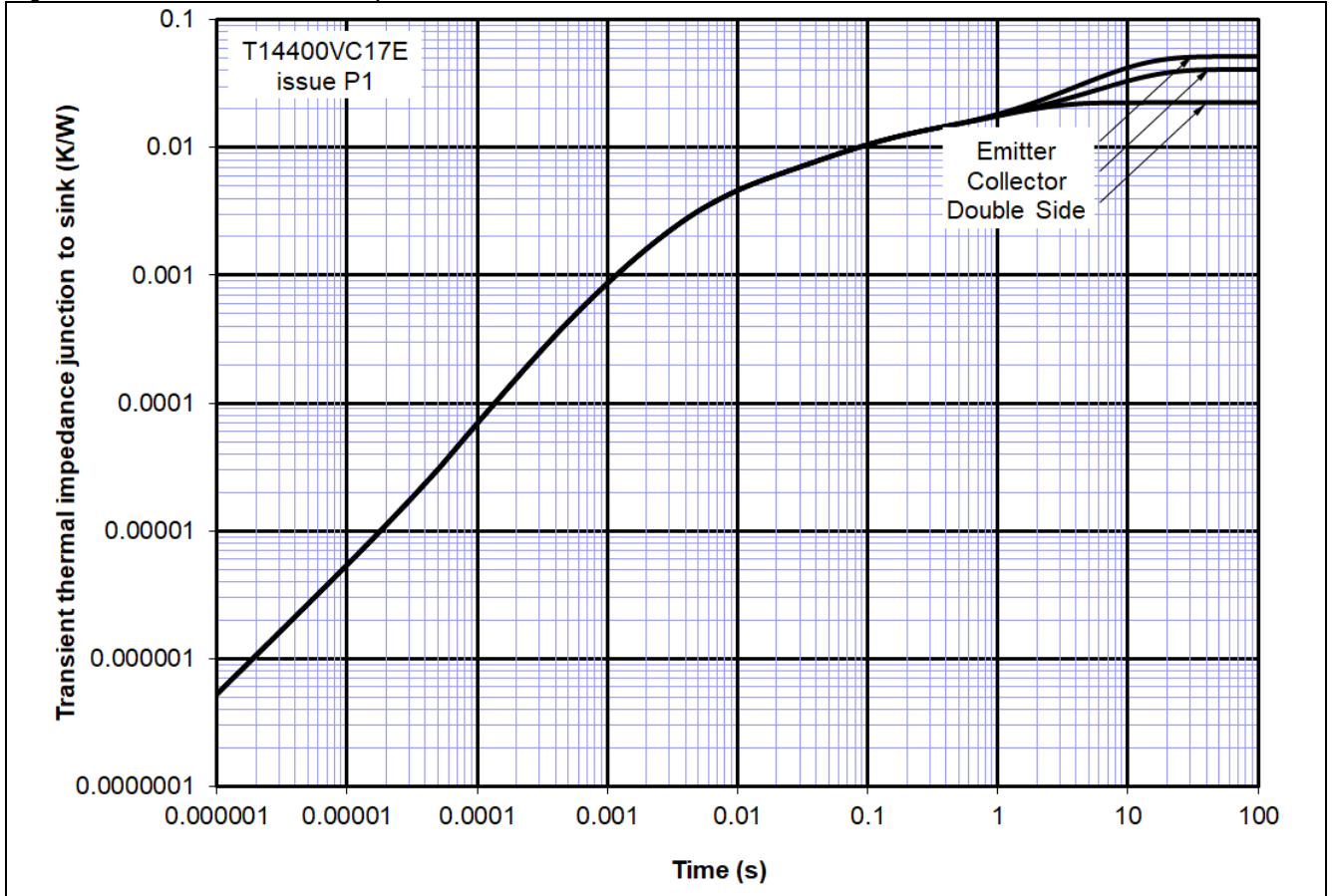
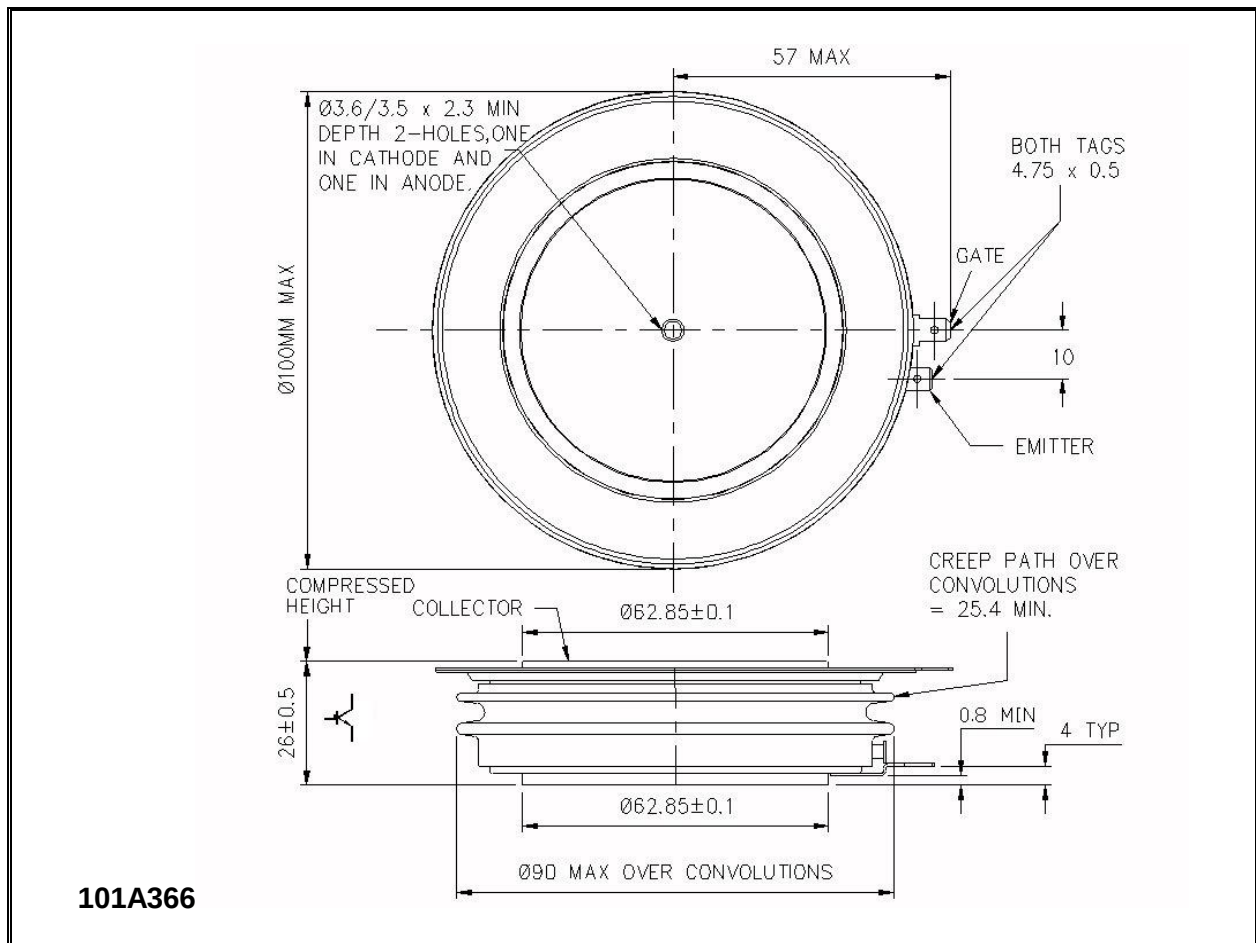


Figure 5 – Transient thermal impedance



Outline Drawing & Ordering Information



ORDERING INFORMATION

(Please quote 10 digit code as below)

T1440	VC	17	E
Fixed type Code	Fixed Outline Code	Voltage Grade $V_{CES}/100$ 17	Fixed format code

 Typical order code: T1440VC17E ($V_{CES} = 1700V$)

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