

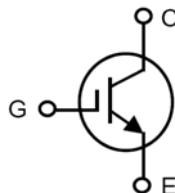
GenX3™ 600V IGBT

(Electrically Isolated Tab)

High-Speed Low-V_{sat} PT IGBT
40-100 kHz Switching

IXGR72N60C3*

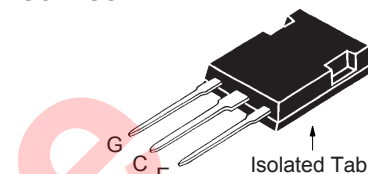
*Obsolete Part Number



V_{CES}	=	600V
I_{C110}	=	35A
$V_{CE(sat)}$	≤	2.7V
$t_{fi(typ)}$	=	55ns

Symbol	Test Conditions	Maximum Ratings	
V_{CES}	$T_J = 25^\circ\text{C}$ to 150°C	600	V
V_{CGR}	$T_J = 25^\circ\text{C}$ to 150°C , $R_{GE} = 1\text{M}\Omega$	600	V
V_{GES}	Continuous	±20	V
V_{GEM}	Transient	±30	V
I_{C25}	$T_C = 25^\circ\text{C}$	80	A
I_{C110}	$T_C = 110^\circ\text{C}$	35	A
I_{CM}	$T_C = 25^\circ\text{C}$, 1ms	400	A
I_A	$T_C = 25^\circ\text{C}$	50	A
E_{AS}	$T_C = 25^\circ\text{C}$	500	mJ
SSOA (RBSOA)	$V_{GE} = 15\text{V}$, $T_{VJ} = 125^\circ\text{C}$, $R_G = 2\Omega$ Clamped Inductive Load	$I_{CM} = 150$ $V_{CE} \leq V_{CES}$	A
P_C	$T_C = 25^\circ\text{C}$	200	W
T_J		-55 ... +150	°C
T_{JM}		150	°C
T_{stg}		-55 ... +150	°C
T_L	Maximum Lead Temperature for Soldering	300	°C
T_{SOLD}	Plastic Body for 10s	260	°C
V_{ISOL}	50/60 Hz, 1 Minute	2500	V~
F_C	Mounting Force	20..120/4.5..27	N/lb
Weight		5	g

ISOPLUS 247™



G = Gate C = Collector
E = Emitter

Features

- Silicon Chip on Direct-Copper Bond (DCB) Substrate
- Optimized for Low Switching Losses
- Square RBSOA
- Isolated Mounting Surface
- Avalanche Rated
- 2500V Electrical Isolation

Advantages

- High Power Density
- Low Gate Drive Requirement

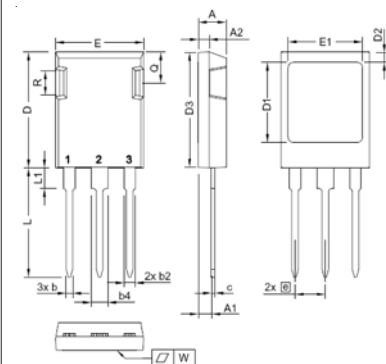
Applications

- High Frequency Power Inverters
- UPS
- Motor Drives
- SMPS
- PFC Circuits
- Battery Chargers
- Welding Machines
- Lamp Ballasts

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
BV_{CES}	$I_C = 250\mu\text{A}$, $V_{GE} = 0\text{V}$	600		V
$V_{GE(th)}$	$I_C = 250\mu\text{A}$, $V_{CE} = V_{GE}$	3.0		5.5 V
I_{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0\text{V}$ $T_J = 125^\circ\text{C}$			300 μA 1 mA
I_{GES}	$V_{CE} = 0\text{V}$, $V_{GE} = \pm 20\text{V}$			±100 nA
$V_{CE(sat)}$	$I_C = 50\text{A}$, $V_{GE} = 15\text{V}$ $T_J = 125^\circ\text{C}$	2.10	2.70	V
		1.65		V

Symbol	Test Conditions ($T_J = 25^\circ\text{C}$, Unless Otherwise Specified)	Characteristic Values		
		Min.	Typ.	Max.
g_{fs}	$I_C = 50\text{A}$, $V_{CE} = 10\text{V}$, Note 1	33	55	S
C_{ies}	$V_{CE} = 25\text{V}$, $V_{GE} = 0\text{V}$, $f = 1\text{MHz}$		4780	pF
C_{oes}			330	pF
C_{res}			117	pF
Q_g	$I_C = 50\text{A}$, $V_{GE} = 15\text{V}$, $V_{CE} = 0.5 \cdot V_{CES}$		175	nC
Q_{ge}			33	nC
Q_{gc}			72	nC
$t_{d(on)}$	Inductive Load, $T_J = 25^\circ\text{C}$		27	ns
t_{ri}			37	ns
E_{on}	$I_C = 50\text{A}$, $V_{GE} = 15\text{V}$		1.03	mJ
$t_{d(off)}$		$V_{CE} = 480\text{V}$, $R_G = 2\Omega$, Note 2		77
t_{fi}			55	110 ns
E_{off}			0.48	0.95 mJ
$t_{d(on)}$	Inductive Load, $T_J = 125^\circ\text{C}$		26	ns
t_{ri}			36	ns
E_{on}	$I_C = 50\text{A}$, $V_{GE} = 15\text{V}$		1.48	mJ
$t_{d(off)}$		$V_{CE} = 480\text{V}$, $R_G = 2\Omega$, Note 2		120
t_{fi}			124	ns
E_{off}			0.93	mJ
R_{thJC}			0.62	$^\circ\text{C/W}$
R_{thCS}		0.15		$^\circ\text{C/W}$

ISOPLUS247 (IXGR) Outline



- 1 - Gate
- 2 - Collector
- 3 - Emitter

Dim.	Millimeter		Inches	
	min	max	min	max
A	4.83	5.21	0.190	0.205
A1	2.29	2.54	0.090	0.100
A2	1.91	2.16	0.075	0.085
b	1.14	1.40	0.045	0.055
b2	1.91	2.20	0.075	0.087
b4	2.92	3.24	0.115	0.128
c	0.61	0.83	0.024	0.033
D	20.80	21.34	0.819	0.840
D1	15.75	16.26	0.620	0.640
D2	1.65	2.15	0.065	0.085
D3	20.30	20.70	0.799	0.815
E	15.75	16.13	0.620	0.635
E1	13.21	13.72	0.520	0.540
e	5.45 BSC		0.215 BSC	
L	19.81	20.60	0.780	0.811
L1	3.81	4.38	0.150	0.172
Q	5.59	6.20	0.220	0.244
R	4.25	5.50	0.167	0.217
W	-	0.10	-	0.004

Notes:

1. Pulse test, $t \leq 300\mu\text{s}$, duty cycle, $d \leq 2\%$.
2. Switching times & energy losses may increase for higher $V_{CE}(\text{Clamp})$, T_J or R_G .

PRELIMINARY TECHNICAL INFORMATION

The product presented herein is under development. The Technical Specifications offered are derived from a subjective evaluation of the design, based upon prior knowledge and experience, and constitute a "considered reflection" of the anticipated result. IXYS reserves the right to change limits, test conditions, and dimensions without notice.

IXYS Reserves the Right to Change Limits, Test Conditions, and Dimensions.

IXYS MOSFETs and IGBTs are covered by one or more of the following U.S. patents:	4,835,592	4,931,844	5,049,961	5,237,481	6,162,665	6,404,065 B1	6,683,344	6,727,585	7,005,734 B2	7,157,338B2
	4,860,072	5,017,508	5,063,307	5,381,025	6,259,123 B1	6,534,343	6,710,405 B2	6,759,692	7,063,975 B2	
	4,881,106	5,034,796	5,187,117	5,486,715	6,306,728 B1	6,583,505	6,710,463	6,771,478 B2	7,071,537	

Fig. 1. Output Characteristics @ $T_J = 25^\circ\text{C}$

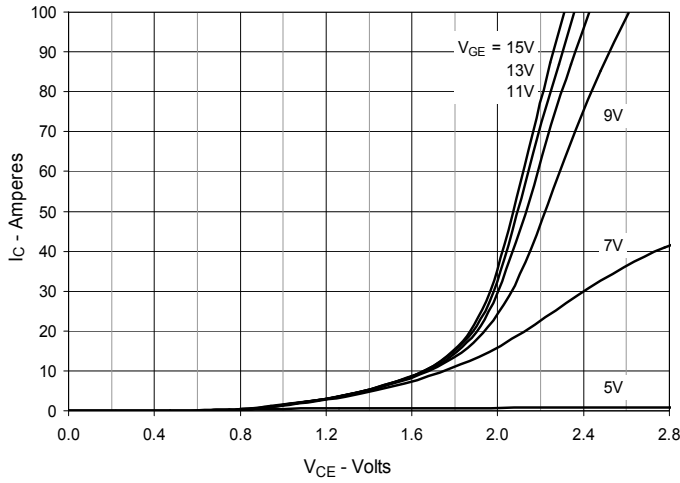


Fig. 2. Extended Output Characteristics @ $T_J = 25^\circ\text{C}$

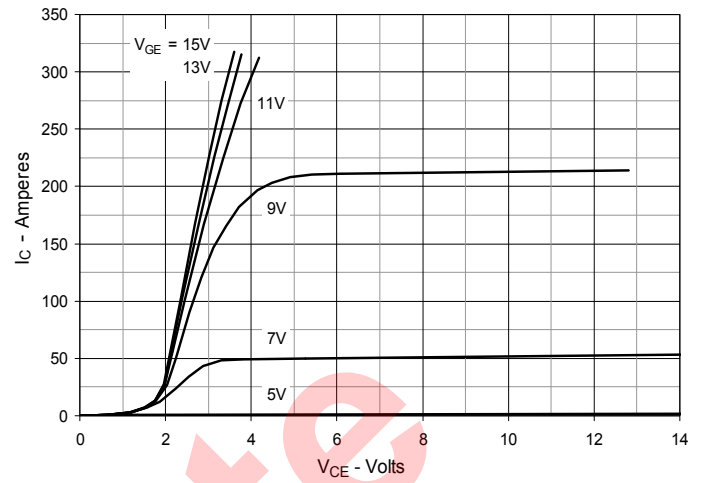


Fig. 3. Output Characteristics @ $T_J = 125^\circ\text{C}$

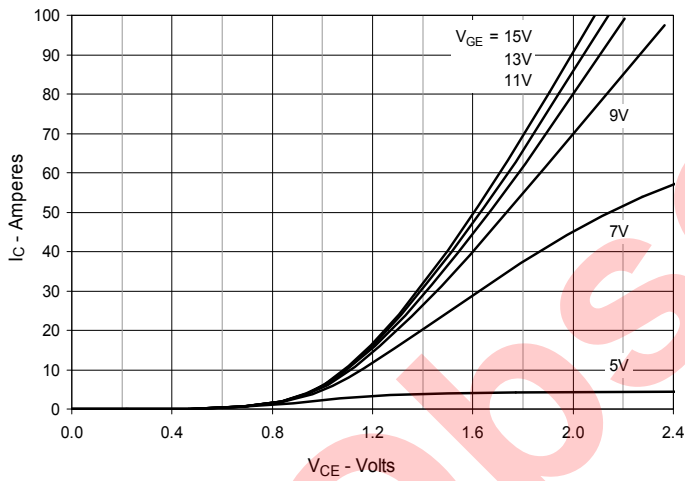


Fig. 4. Dependence of $V_{CE(sat)}$ on Junction Temperature

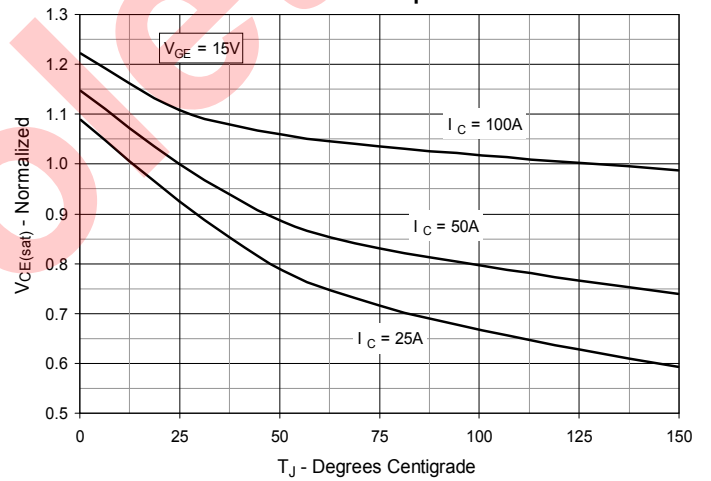


Fig. 5. Collector-to-Emitter Voltage vs. Gate-to-Emitter Voltage

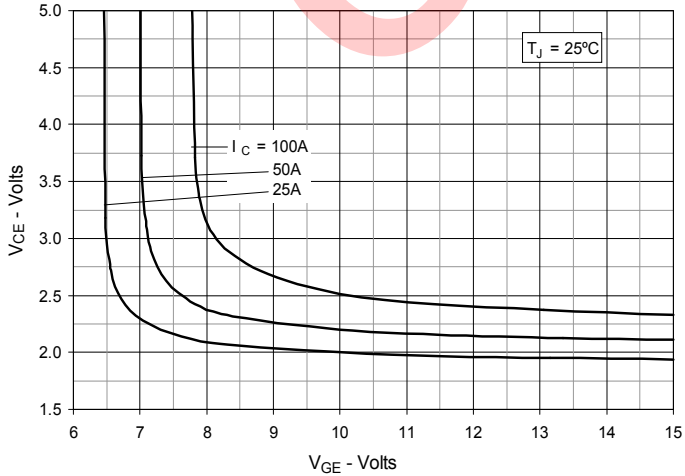


Fig. 6. Input Admittance

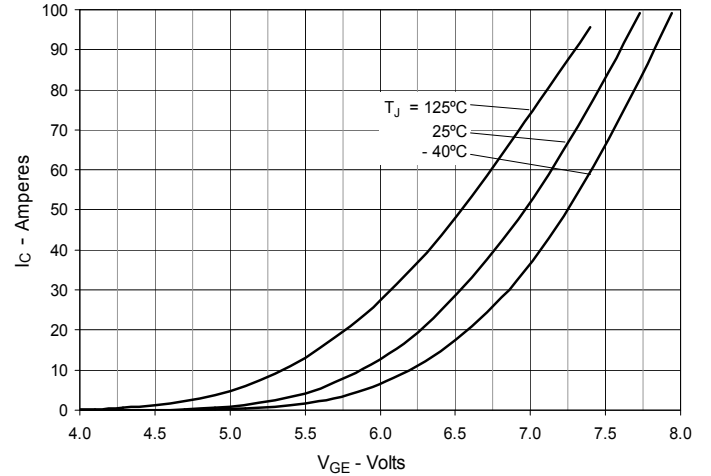


Fig. 7. Transconductance

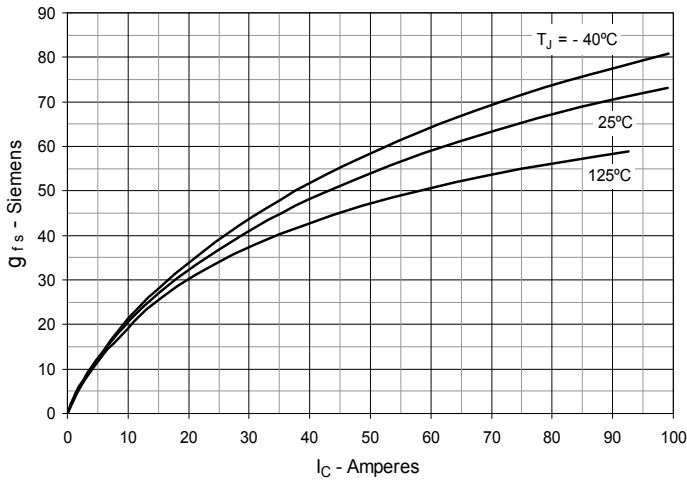


Fig. 8. Gate Charge

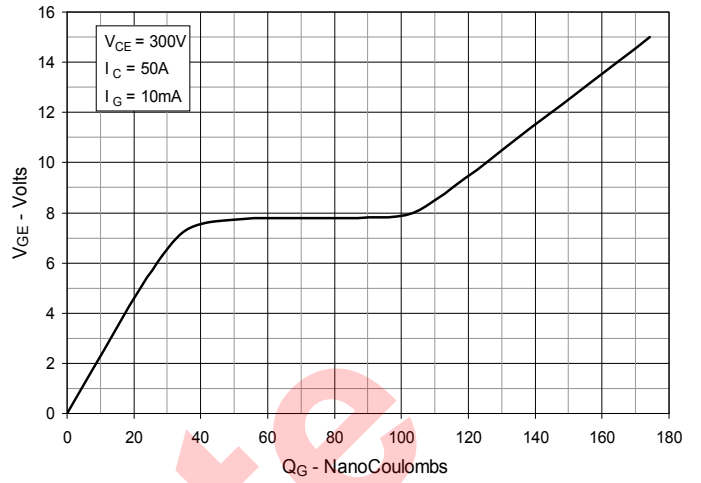


Fig. 9. Capacitance

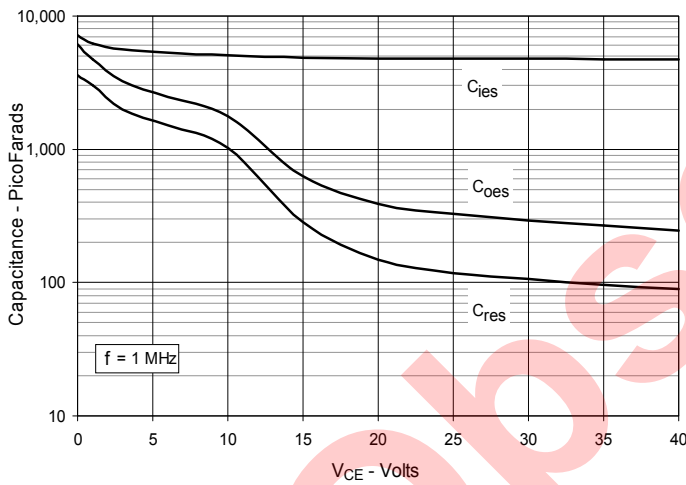


Fig. 10. Reverse-Bias Safe Operating Area

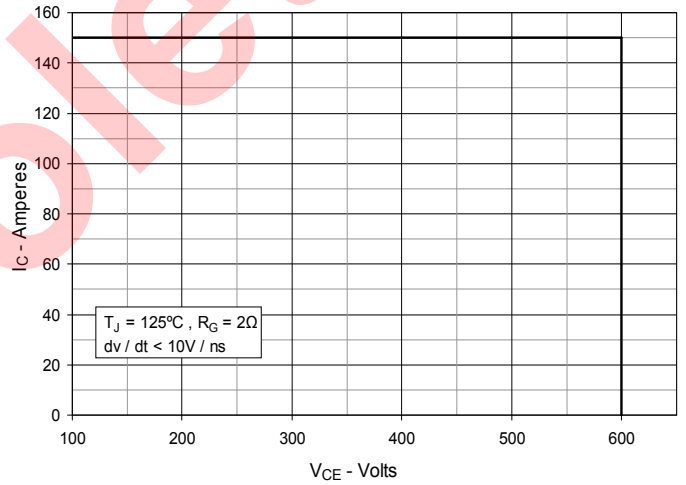
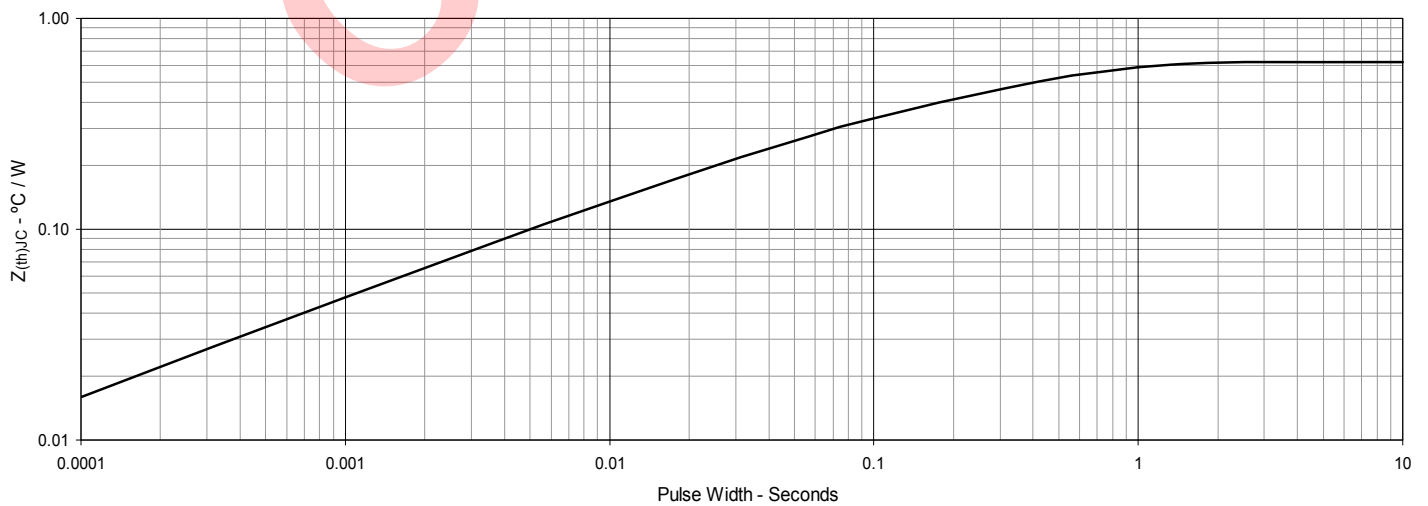
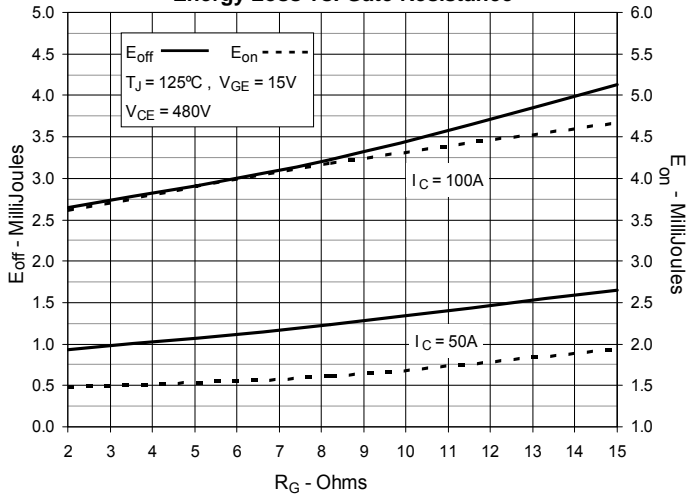
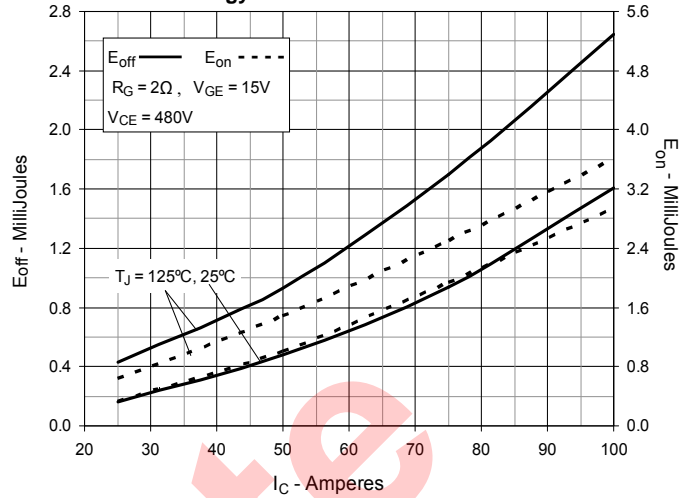
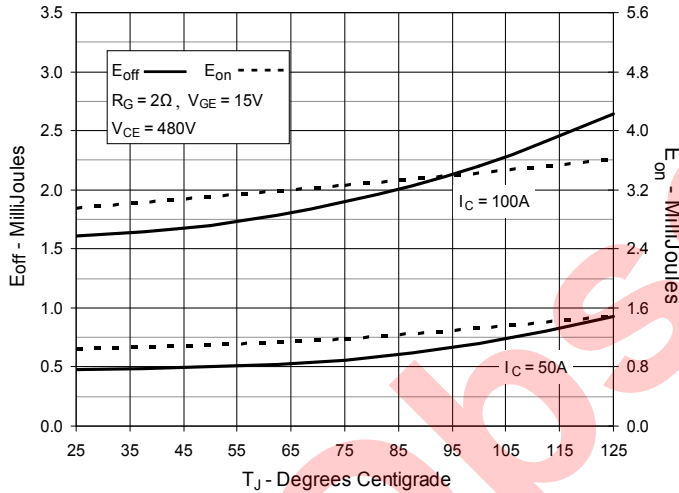
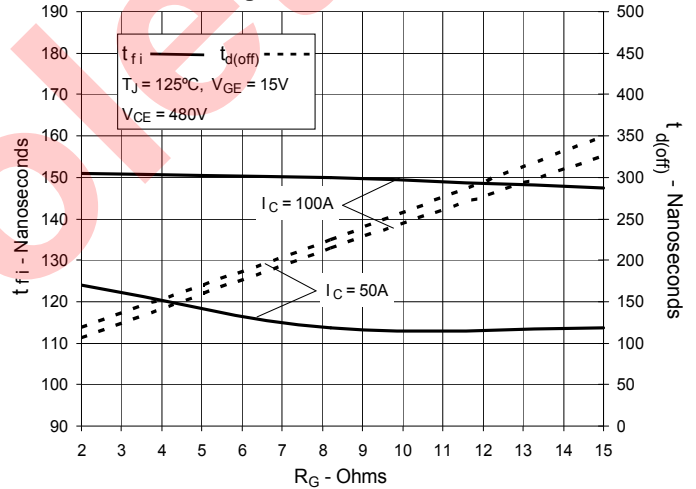
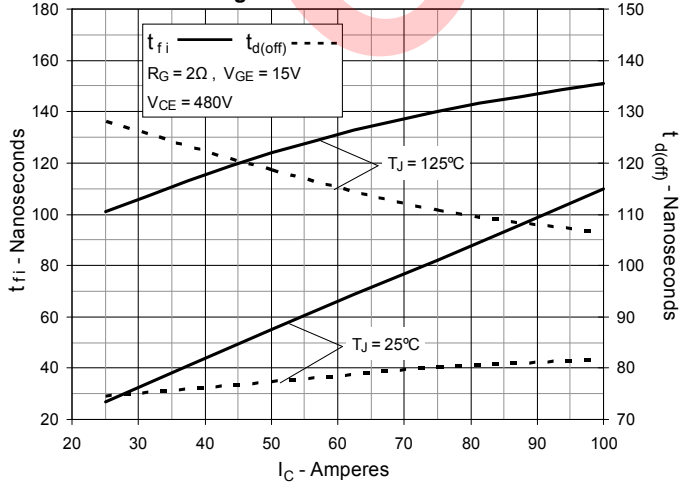
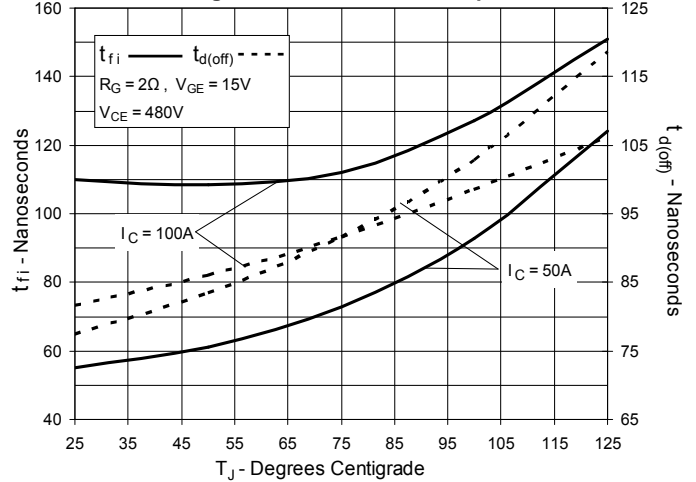
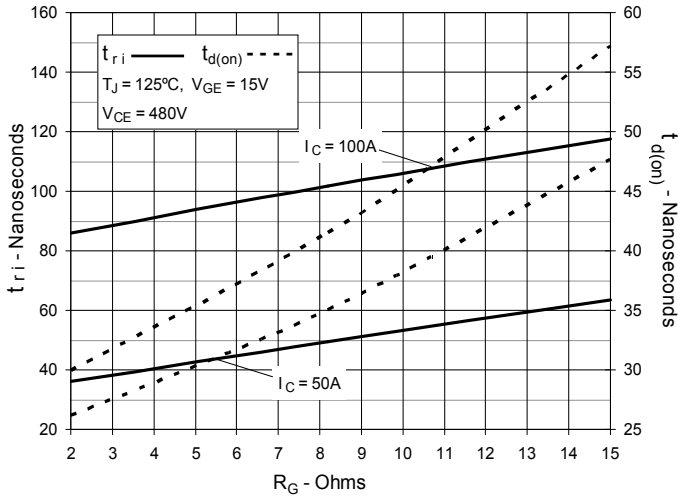


Fig. 11. Maximum Transient Thermal Impedance

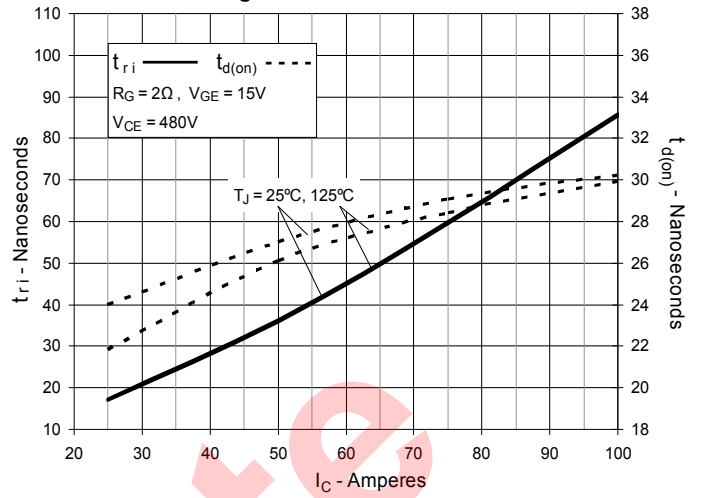


**Fig. 12. Inductive Switching
Energy Loss vs. Gate Resistance**

**Fig. 13. Inductive Switching
Energy Loss vs. Collector Current**

**Fig. 14. Inductive Switching
Energy Loss vs. Junction Temperature**

**Fig. 15. Inductive Turn-off
Switching Times vs. Gate Resistance**

**Fig. 16. Inductive Turn-off
Switching Times vs. Collector Current**

**Fig. 17. Inductive Turn-off
Switching Times vs. Junction Temperature**


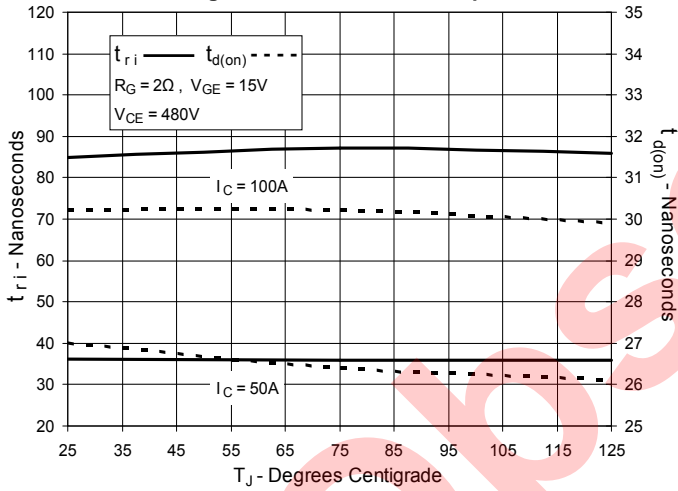
**Fig. 18. Inductive Turn-on
Switching Times vs. Gate Resistance**



**Fig. 19. Inductive Turn-on
Switching Times vs. Collector Current**



**Fig. 20. Inductive Turn-on
Switching Times vs. Junction Temperature**



Obsolete



IXYS
A Littelfuse Technology

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