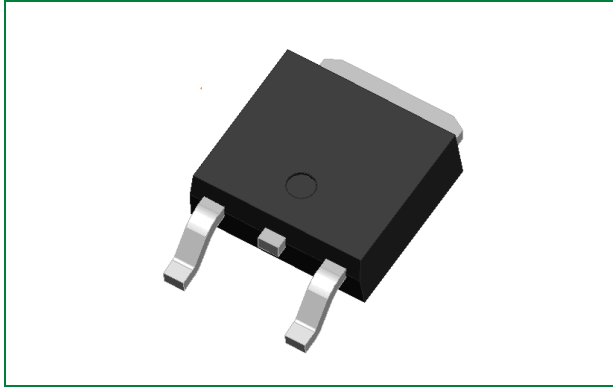


LGD8201ATI

400 V, 20 A N-Channel Ignition IGBT



Product Summary

Characteristic	Value	Unit
V_{CES}	400	V
I_c	20	A

Description

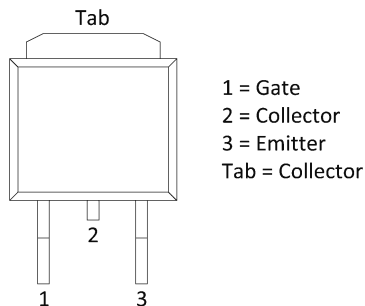
This Logic Level Insulated Gate Bipolar Transistor (IGBT) features monolithic circuitry integrating ESD and Over-Voltage clamped protection for use in inductive coil drivers applications. Primary uses include Ignition, Direct Fuel Injection, or wherever high voltage and high current switching is required.

Agency Approvals

Environmental Approvals



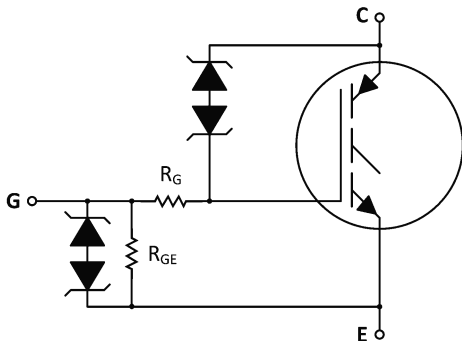
Pinout Diagram



Features

- Ideal for Coil-on-Plug and Driver-on-Coil Applications
- DPAK Package Offers Smaller Footprint for Increased Board Space
- Gate-Emitter ESD Protection
- Temperature Compensated Gate-Collector Voltage Clamp Limits Stress Applied to Load
- Integrated ESD Diode Protection
- Low Threshold Voltage Interfaces Power Loads to Logic or Microprocessor Devices
- Low Saturation Voltage
- High Pulsed Current Capability
- AEC-Q101 Qualified
- These are Pb-Free Devices

Functional Diagram



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1. Maximum Ratings (T_J = 25 °C unless otherwise specified)

Characteristic	Conditions	Symbol	Value	Unit
Collector-Emitter Voltage	-	V _{CEs}	440	V
Collector-Gate Voltage	-	V _{CER}	440	V
Gate-Emitter Voltage	-	V _{GE}	±15	V
Collector Current – Continuous	T _C = 25 °C	I _C	20	A _{DC}
Collector Current – Pulsed			50	A _{AC}
Continuous Gate Current	-	I _G	1.0	mA
Transient Gate Current	t ≤ 2 ms, f ≤ 100 Hz		20	mA
ESD – Charged Device Model	-	ESD	2.0	kV
ESD – Human Body Model	R = 1500 Ω, C = 100 pF		8.0	kV
ESD – Machine Model	R = 0 Ω, C = 200 pF		500	V
Total Power Dissipation	T _C = 25 °C	P _D	125	W
	Derating for > 25 °C		0.83	W/°C
Operating and Storage Temperature Range	-	T _J , T _{stg}	-55 to +175	°C

2. Unclamped Collector-to-Emitter Avalanche Characteristics

Characteristic	Symbol	Value	Unit
Single Pulse Collector-to-Emitter Avalanche Energy			
V _{CC} = 50 V, V _{GE} = 5.0 V, P _{kL} = 16.7 A, R _G = 1000 Ω, L = 1.8 mH, Starting T _J = 25 °C	E _{AS}	250	mJ
V _{CC} = 50 V, V _{GE} = 5.0 V, P _{kL} = 14.9 A, R _G = 1000 Ω, L = 1.8 mH, Starting T _J = 150 °C		200	
V _{CC} = 50 V, V _{GE} = 5.0 V, P _{kL} = 14.1 A, R _G = 1000 Ω, L = 1.8 mH, Starting T _J = 175 °C		180	
Reverse Avalanche Energy			
V _{CC} = 100 V, V _{GE} = 20 V, P _{kL} = 25.8 A, L = 6.0 mH, Starting T _J = 25 °C	E _{AS(R)}	2000	mJ

3. Thermal Characteristics

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction to Case	R _{θJC}	1.2	°C/W
Thermal Resistance, Junction to Ambient (DPAK) ¹	R _{θJA}	95	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 5 seconds	T _L	275	°C

Footnote 1: When surface mounted to an FR4 board using the minimum recommended pad size

4. Electrical Characteristics – Off

Characteristic	Symbol	Conditions	Temperature	Value			Unit
				Min	Typ	Max	
Collector-Emitter Clamp Voltage	BV _{CES}	I _C = 2.0 mA	T _J = -40 °C to 175 °C	370	395	420	V
		I _C = 10 mA		390	415	440	
Zero Gate Voltage Collector Current	I _{CES}	V _{CE} = 15 V, V _{GE} = 0 V	T _J = 25 °C	-	0.1	1.0	μA
			T _J = 175 °C	0.5	1.5	10	
		V _{CE} = 200 V, V _{GE} = 0 V	T _J = 175 °C	1.0	25	100 ²	
			T _J = -40 °C	0.4	0.8	5.0	
Reverse Collector-Emitter Leakage Current	I _{CES(R)}	V _{CE} = -24 V	T _J = 25 °C	0.05	0.2	1.0	mA
			T _J = 175 °C	1.0	8.5	25	
			T _J = -40 °C	0.005	0.025	0.2	
Reverse Collector-Emitter Clamp Voltage	BV _{CES(R)}	I _C = -75 mA	T _J = 25 °C	30	35	39	V
			T _J = 175 °C	35	39	45 ²	
			T _J = -40 °C	30	33	37	
Gate-Emitter Clamp Voltage	BV _{GES}	I _G = ±5.0 mA	T _J = -40 °C to 175 °C	12	12.5	14	V
Gate-Emitter Leakage Current	I _{GES}	V _{GE} = ±5.0 V	T _J = -40 °C to 175 °C	200	300	350 ²	μA
Gate Resistor	R _G	-	T _J = -40 °C to 175 °C	-	70	-	Ω
Gate-Emitter Resistor	R _{GE}	-	T _J = -40 °C to 175 °C	14.25	16	25	kΩ

Footnote 2: Maximum value of characteristic across temperature range

5. Electrical Characteristics – On

Characteristic	Symbol	Conditions	Temperature	Value			Unit
				Min	Typ	Max	
Gate Threshold Voltage	V _{GE(th)}	I _C = 1.0 mA, V _{GE} = V _{CE}	T _J = 25 °C	1.5	1.8	2.1	V
			T _J = 175 °C	0.7	1.0	1.3	
			T _J = -40 °C	1.7	2.0	2.3 ²	
Threshold Temperature Coefficient (Negative)	-	-	-	4.0	4.6	5.2	mV/°C
Collector-Emitter On-Voltage ³	V _{CE(on)}	I _C = 6.5 A, V _{GE} = 3.7 V	T _J = 25 °C	0.85	1.03	1.35	V
			T _J = 175 °C	0.7	0.9	1.15	
			T _J = -40 °C	0.9	1.11	1.4	
		I _C = 9.0 A, V _{GE} = 3.9 V	T _J = 25 °C	0.9	1.11	1.45	
			T _J = 175 °C	0.8	1.01	1.25	
			T _J = -40 °C	1.0	1.18	1.5	
		I _C = 7.5 A, V _{GE} = 4.5 V	T _J = 25 °C	0.85	1.15	1.4	
			T _J = 175 °C	0.7	0.95	1.2	
			T _J = -40 °C	1.0	1.3	1.6 ²	
		I _C = 10 A, V _{GE} = 4.5 V	T _J = 25 °C	1.0	1.3	1.6	
			T _J = 175 °C	0.8	1.05	1.4	
			T _J = -40 °C	1.1	1.4	1.7 ²	
		I _C = 15 A, V _{GE} = 4.5 V	T _J = 25 °C	1.15	1.45	1.7	
			T _J = 175 °C	1.0	1.3	1.55	
			T _J = -40 °C	1.25	1.55	1.8 ²	
I _C = 20 A, V _{GE} = 4.5 V	T _J = 25 °C	1.1	1.4	1.9			
	T _J = 175 °C	1.2	1.5	1.8			
	T _J = -40 °C	1.3	1.42	2.0			
Forward Transconductance	gfs	V _{CS} = 5.0 V, I _C = 6.0 A	T _J = 25 °C	10	18	25	Mhos

Footnote 2: Maximum value of characteristic across temperature range

Footnote 3: Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%

6. Dynamic Characteristics

Characteristic	Symbol	Conditions	Temperature	Value			Unit
				Min	Typ	Max	
Input Capacitance	C_{ISS}	$V_{CC} = 25\text{ V}$, $f = 10\text{ kHz}$	$T_J = 25\text{ }^\circ\text{C}$	1100	1300	1500	pF
Output Capacitance	C_{OSS}			70	80	90	
Transfer Capacitance	C_{RSS}			18	20	22	

7. Switching Characteristics

Characteristic	Symbol	Conditions	Temperature	Value			Unit	
				Min	Typ	Max		
Turn-off Delay Time (Resistive)	$t_{d(off)}$	$V_{CC} = 300\text{ V}$, $I_C = 9.0\text{ A}$, $R_G = 1.0\text{ k}\Omega$, $R_L = 33\text{ }\Omega$, $V_{GE} = 5.0\text{ V}$	$T_J = 25\text{ }^\circ\text{C}$	6.0	8.0	10	μs	
			$T_J = 175\text{ }^\circ\text{C}$	6.0	8.0	10		
Fall Time (Resistive)	t_f		$T_J = 25\text{ }^\circ\text{C}$	4.0	6.0	8.0	μs	
			$T_J = 175\text{ }^\circ\text{C}$	8.0	10.5	14		
Turn-off Delay Time (Inductive)	$t_{d(off)}$	$V_{CC} = 300\text{ V}$, $I_C = 9.0\text{ A}$, $R_G = 1.0\text{ k}\Omega$, $L = 300\text{ }\mu\text{H}$, $V_{GE} = 5.0\text{ V}$	$T_J = 25\text{ }^\circ\text{C}$	3.0	5.0	7.0	μs	
			$T_J = 175\text{ }^\circ\text{C}$	5.0	7.0	9.0		
Fall Time (Inductive)	t_f		$T_J = 25\text{ }^\circ\text{C}$	1.5	3.0	4.5	μs	
			$T_J = 175\text{ }^\circ\text{C}$	5.0	7.0	10		
Turn-on Delay Time	$t_{d(on)}$		$V_{CC} = 14\text{ V}$, $I_C = 9.0\text{ A}$, $R_G = 1.0\text{ k}\Omega$, $R_L = 1.5\text{ }\Omega$, $V_{GE} = 5.0\text{ V}$	$T_J = 25\text{ }^\circ\text{C}$	1.0	1.5	2.0	μs
				$T_J = 175\text{ }^\circ\text{C}$	1.0	1.5	2.0	
Rise Time	t_r	$T_J = 25\text{ }^\circ\text{C}$		4.0	6.0	8.0	μs	
		$T_J = 175\text{ }^\circ\text{C}$		3.0	5.0	7.0		

8. Figure Data

Figure 1. Self-Clamped Inductive Switching

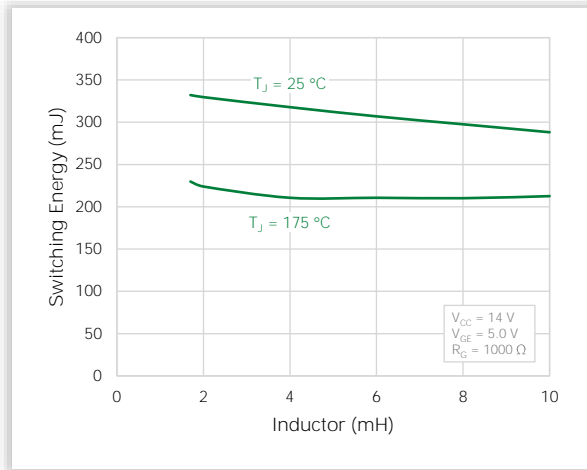


Figure 2. Open Secondary Avalanche Current vs. Temperature

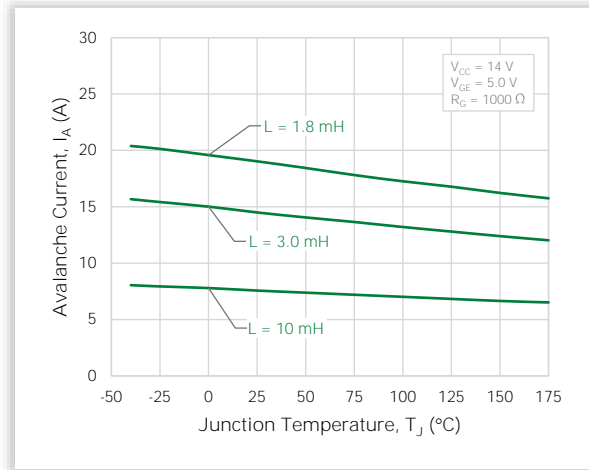


Figure 3. Collector-Emitter Voltage vs. Junction Temperature

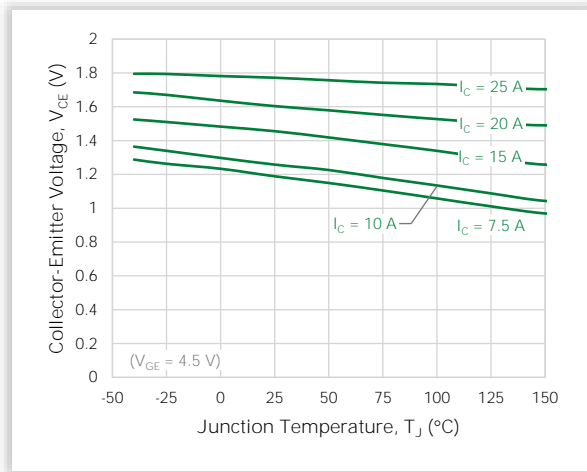
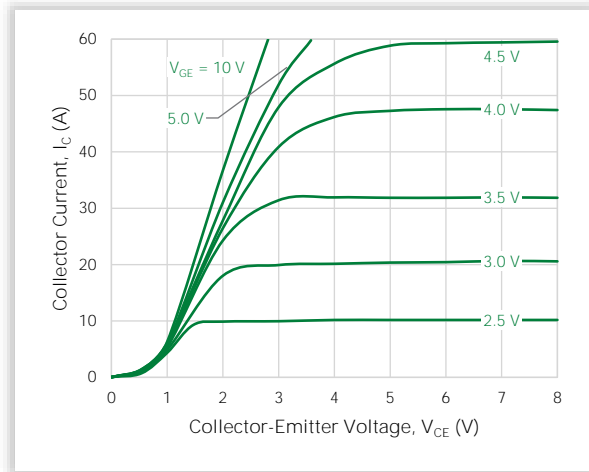
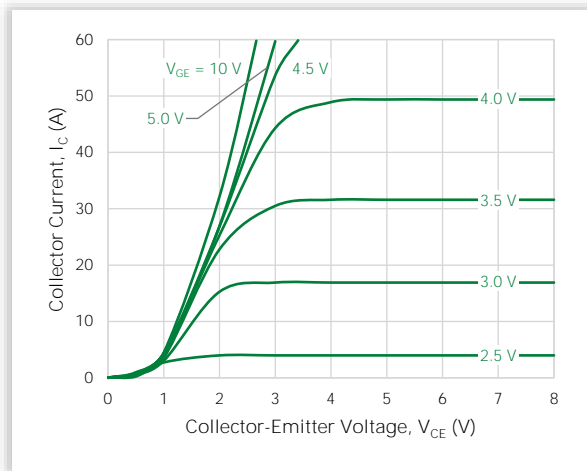
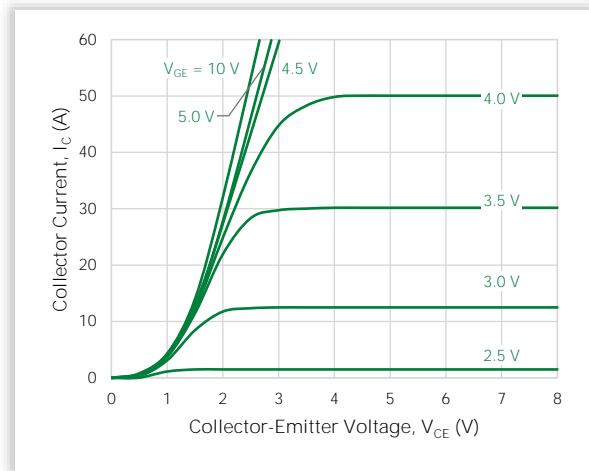

 Figure 4. Output Characteristics (T_J = 175 °C)

 Figure 5. Output Characteristics (T_J = 25 °C)

 Figure 6. Output Characteristics (T_J = -40 °C)


Figure 7. Transfer Characteristics

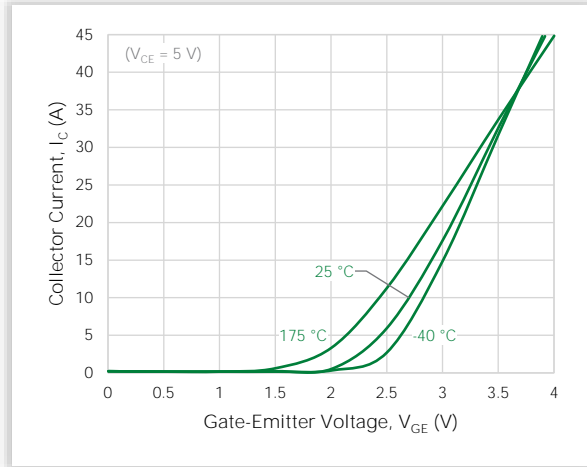


Figure 8. Collector-Emitter Leakage Current vs. Temperature

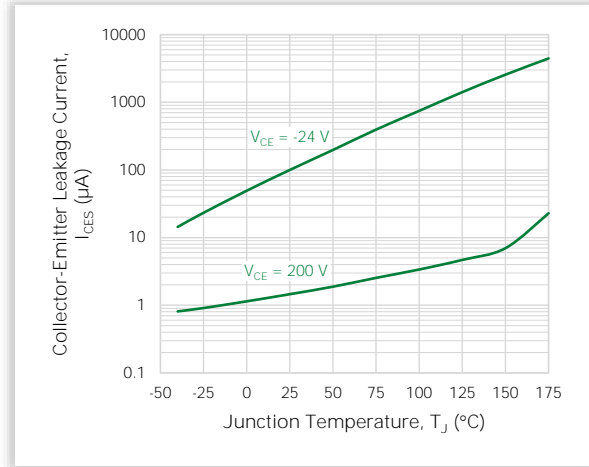


Figure 9. Gate Threshold Voltage vs. Temperature

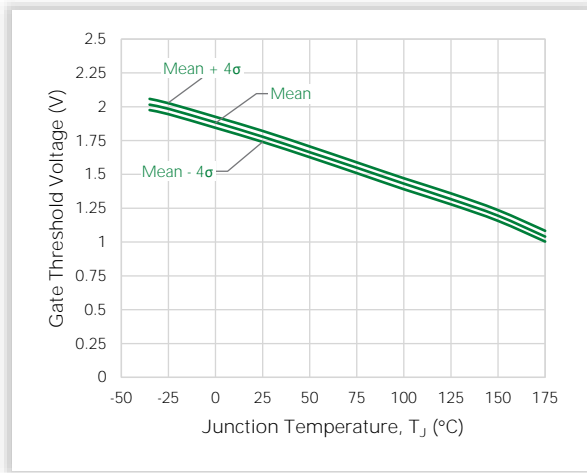


Figure 10. Capacitance Variance

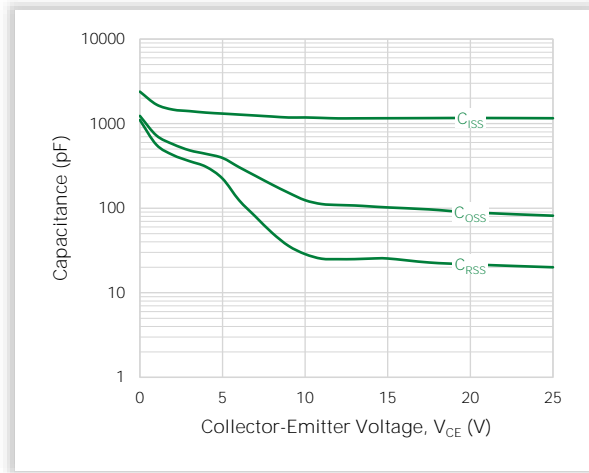


Figure 11. Resistive Switching Fall Time vs. Temperature

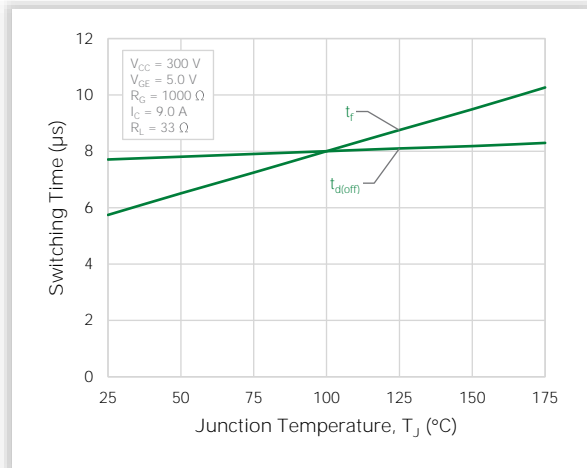


Figure 12. Inductive Switching Fall Time vs. Temperature

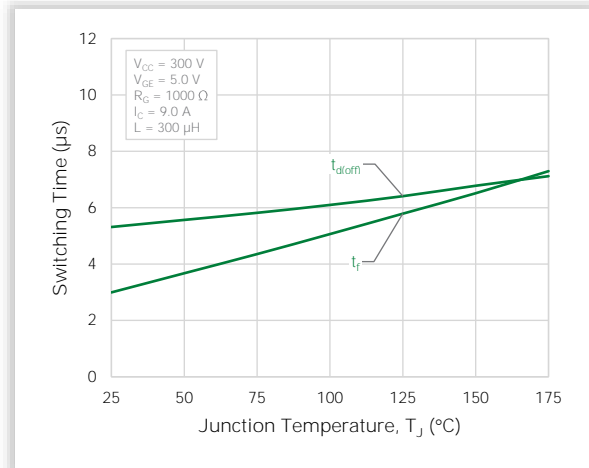


Figure 13. Minimum Pad Transient Thermal Resistance
(Non-normalized Junction-Ambient)

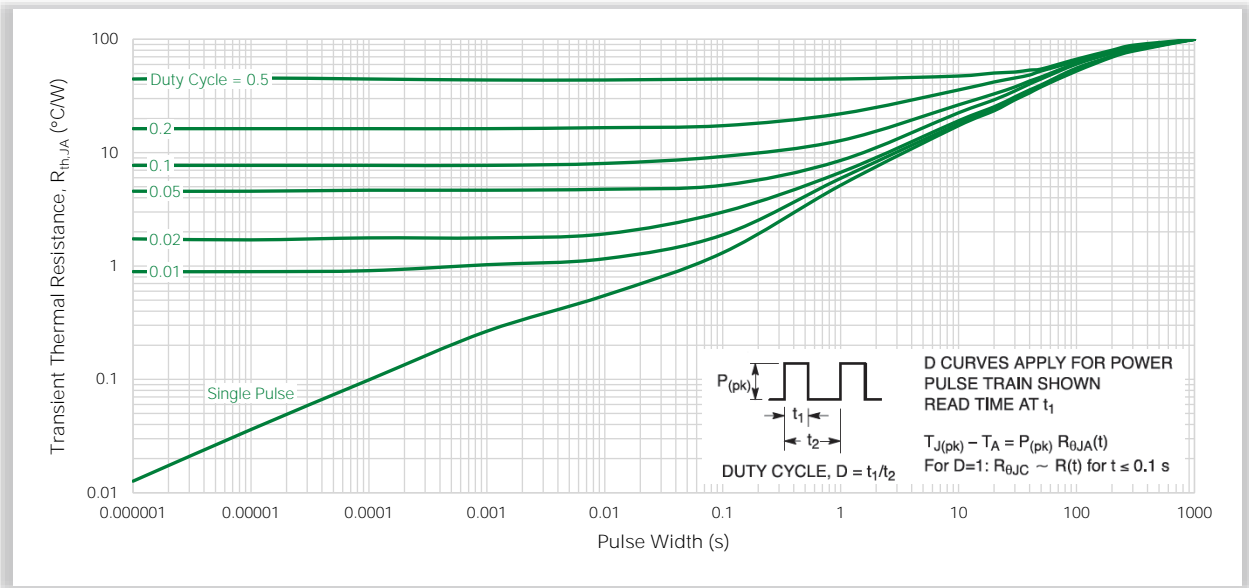
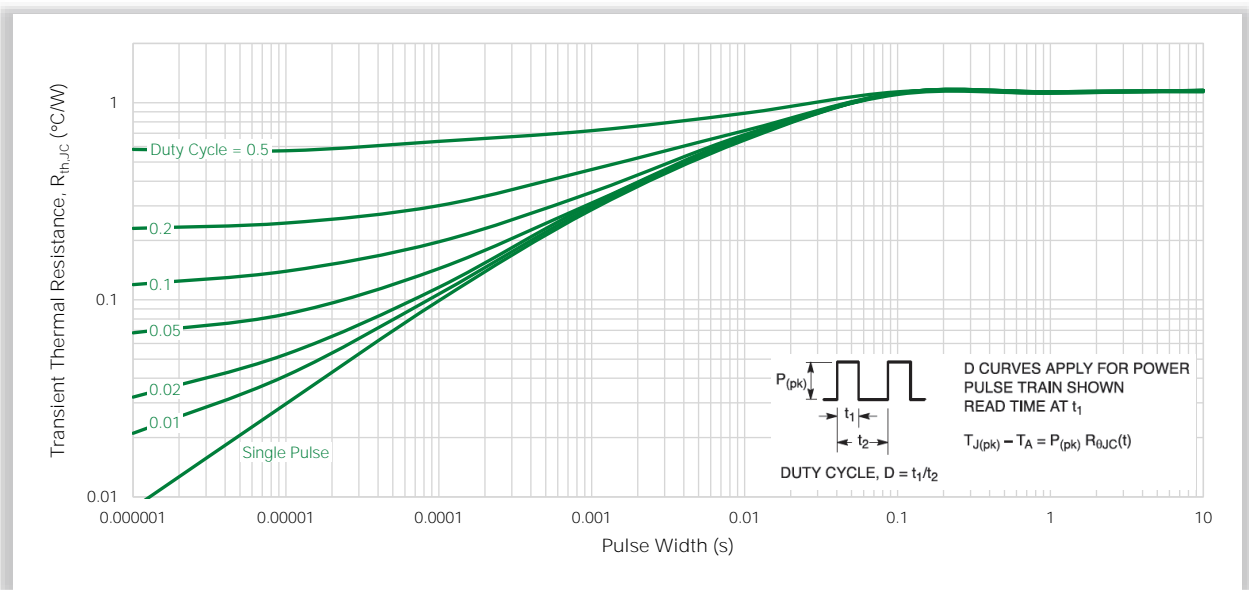
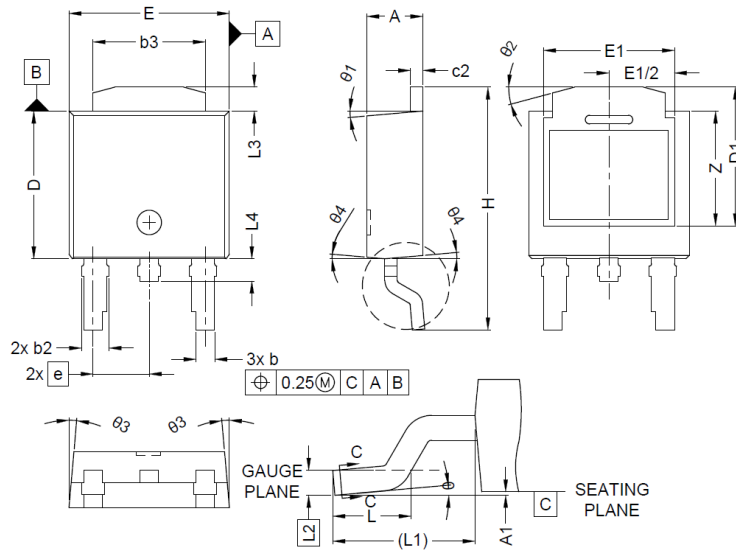


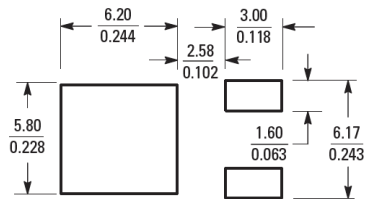
Figure 14. Best Case Transient Thermal Resistance
(Non-normalized Junction-Case mounted on cold plate)



9. Package Dimensions



Recommended Solder Pad Layout:



Notes:

1. DIMENSIONING & TOLERANCEING CONFIRM TO ASME Y14.5M-1994.
2. ALL DIMENSIONS ARE IN MILLIMETERS. ANGLES ARE IN DEGREES.
3. HEAT SINK SIDE FLASH IS MAX. 0.8mm .
4. RADIUS ON TERMINAL IS OPTIONAL.

Symbol	Millimeters		
	Min	Nom	Max
A	2.18	-	2.38
A1	0.00	-	0.13
b	0.63	-	0.89
b2	0.72	-	1.14
b3	4.57	-	5.46
c	0.46	-	0.61
c2	0.46	-	0.61
D	5.97	-	6.22
D1	5.45	-	5.85
E	6.35	-	6.73
E1	5.14	-	5.54
e	2.29 BSC		
H	9.40	-	10.41
L	1.40	-	1.78
L1	2.90 REF		
L2	0.51 BSC		
L3	0.89	-	1.27
L4	-	-	1.01
Z	3.93	-	-
θ	0°	-	10°
θ1	0°	-	10°
θ2	10°	-	20°
θ3	0°	-	10°
θ4	0°	-	10°

10. Part Numbering and Marking



GD8201A = Device Code
 A = Assembly Location
 XX = Lot Number
 Y = Year
 WW = Work Week

11. Packing Options

Part Number	Package	Packing Mode	M.O.Q.
LGD8201ATI	DPAK (Pb-Free)	Tape & Reel	2500

For additional information please visit www.Littelfuse.com/powersemi

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