

IX9907 Design Considerations



1 Introduction

This application note provides general guidelines for designing an off-line LED driver using IXYS Integrated Circuits Division's IX9907. The IX9907 integrates a very efficient internal power MOSFET, which greatly simplifies the design of a quasi-resonant flyback converter driver that provides a constant current output to an LED string while maintaining a high power factor.

The IX9907 includes multiple safety features such as under-voltage lockout, over-voltage protection, over-temperature protection, digital soft-start, foldback correction, and cycle-by-cycle peak current limiting. This driver is an excellent choice for many phase-cut dimming and high power factor correction LED lighting applications demanding compact design.

Figure 1 IX9907 Block Diagram

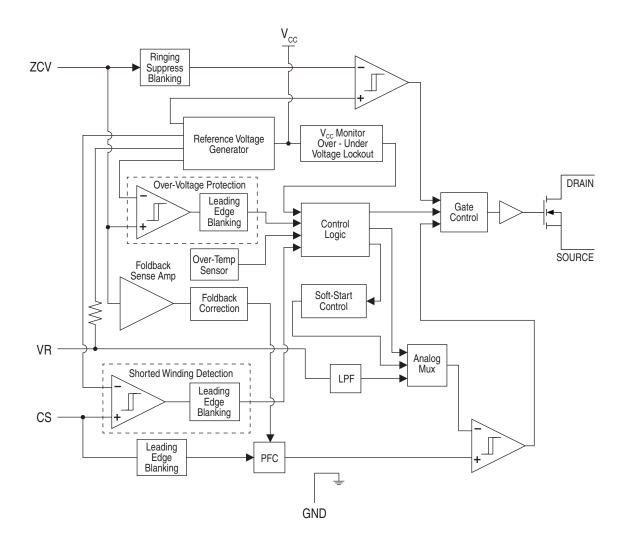
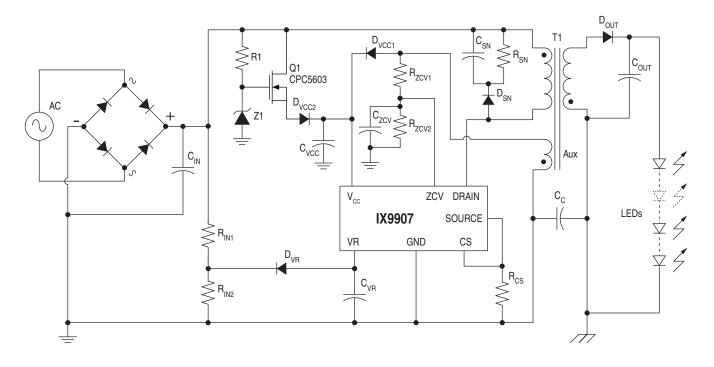




Figure 2 IX9907 Application Circuit



1.1 LED Driver Specifications

The following equations and component selections are based on the following LED driver specifications:

Parameter	Symbol	Rating	Units	
AC Input Voltage				
Minimum Voltage	V_{AC_min}	90	V _{rms}	
Maximum Voltage	V _{AC_max}	135	vrms	
AC Input Frequency	f _{AC}	60	Hz	
Auxiliary Voltage	V _{aux_max}	18	V	
Auxiliary Current	l _{aux_max}	30	mA	
LED String Voltage	V _{LEDstring}	20	V	
LED String Current	I _{LED_max}	500	mA	
Estimated Efficiency	η	85	%	
Oscillator Frequency	f _S	67	kHz	
Power Factor	PF	98	%	
Maximum Duty Cycle	D _{max}	50	%	



2 Component Selection

2.1 DC Bulk Voltage at Low and High Line

$$\begin{split} V_{\mathrm{DC_bulk_min}} &= \sqrt{2} \bullet V_{\mathrm{AC_min}} = \sqrt{2} \bullet 90 V_{\mathrm{AC}} \approx 127.3 \, V \\ V_{\mathrm{DC_bulk_max}} &= \sqrt{2} \bullet V_{\mathrm{AC_max}} = \sqrt{2} \bullet 135 V_{\mathrm{AC}} \approx 191 \, V \end{split}$$

2.2 Output Power Calculation

$$P_{\text{out}} = V_{\text{LEDstring}} \bullet I_{\text{LED_max}}$$

 $P_{\text{out}} = 20V \bullet 500 mA = 10W$

2.3 Input Power Calculation

$$P_{\text{in}} = \frac{P_{\text{out}}}{\eta \bullet \text{PF}}$$

$$P_{\text{in}} = \frac{10W}{0.85 \bullet 0.98} \approx 12W$$

2.4 Primary Inductor Currents Calculation

$$I_{\mathrm{pri_peak}} = \frac{\sqrt{2} \bullet V_{\mathrm{AC_min}} \bullet D_{\mathrm{max}}}{L_{\mathrm{pri}} \bullet f_{\mathrm{S}}}$$

$$I_{\mathrm{pri_peak}} = \frac{127.3 \, V \bullet 0.5}{1.82 \, mH \bullet 67 \, kHz} \approx 0.522 A_{\mathrm{peak}}$$

For L_{Dri} , see 3.1 Primary Inductance Calculation.

$$I_{\rm rms} = I_{\rm pri_peak} \bullet \sqrt{\frac{D_{\rm max}}{3}}$$

$$I_{\rm rms} = 0.522 A_{\rm peak} \bullet \sqrt{\frac{0.5}{3}} \approx 0.213 A_{\rm rms}$$

Note that duty cycle above 50% will result in converter stability issues such as sub-harmonic oscillations.

2.5 Reflected Flyback Voltage Calculation

$$V_{\rm ro} = \frac{V_{\rm LEDstring} + V_{\rm f}}{\left(\frac{N_{\rm S}}{N_{\rm p}}\right)} = \frac{20V + 0.7V}{0.1626} \approx 127V$$

See 3.2 Determine Transformer Turns Ratio for N_S/N_P ratio.

2.6 Input Capacitor, C_{IN}

The IX9907 is designed to operate without the need of a large bulk capacitor. This operating method enables the input current to form a triangular shape that closely follows the rectified AC line voltage, enabling a very high power factor of up to 98% with low total harmonic distortion (THD).

A small, 0.1μ F, 400V polyester film filter capacitor should be adequate for this application.

2.7 Input Rectifier Bridge

The breakdown voltage for this bridge is based on the maximum input voltage.

$$V_{\text{bridge}} = \sqrt{2} \bullet 135 V_{\text{AC}} \bullet 2 \approx 382 V$$

Select the next higher standard voltage, 400V.

The rectifier bridge is exposed to high surge currents, so select a bridge that can handle at least five times the I_{rms} input current. In this case, a 1A bridge would be sufficient.

2.8 Calculate MOSFET Voltage Rating

The internal power MOSFET is rated at 650V, 2Ω , so care should be taken not to exceed maximum rating.

Maximum V_{ds} can be approximated as follows:

$$V_{ds} = V_{\text{dc_bulk_max}} + V_{ro} + V_{\text{spike}}$$
$$V_{ds} = 191 V + 127 V + 50 V$$
$$V_{ds} = 368 V$$

In cases where calculated $V_{\rm ds}$ is very close to 650V, and where more margin is desired, reducing reflecting flyback voltage by lowering duty cycle may be one solution. In some cases, reduction of transformer leakage inductance can lower inductive spikes.

The IX9907 internal power MOSFET has an effective output capacitance, $C_{O(er)}$ =22pF plus an additional 44pF due to external components, and an $R_{ds(on)}$ of 4Ω maximum at T_j =125°C. Calculate switching and conduction losses with this formula:

$$\begin{split} P_{SW} &= 0.5 \bullet C_O \bullet V_{\text{dc_bulk_min}}^2 \bullet f_{SW} \\ P_{SW} &= 0.5 \bullet 66pF \bullet 127.3^2 \bullet 67kHz \\ P_{SW} &= 35.83mW \end{split}$$

Conduction losses can be calculated:

$$\begin{split} P_{con} &= \frac{1}{3} \bullet R_{\rm ds(on)} \bullet I_{\rm pri-peak}^{\quad \ 2} \bullet D_{max} \\ P_{con} &= \frac{1}{3} \bullet 4\Omega \bullet I_{\rm pri-peak}^{\quad \ 2} \bullet 0.5 \\ P_{con} &= 182 mW \end{split}$$

Total Power Loss =
$$P_{SW} + P_{con}$$

Total Power Loss = 217.83 mW



The IX9907 data sheet lists Θ_{JA} as 125°C/W; therefore the temperature rise due to the power MOSFET can be estimated. In this case T_A =50°C.

$$T_j = (217.83 \, mW \cdot 125 \, ^{\circ}\text{C/W}) + 50 \, ^{\circ}\text{C}$$

 $T_j = 77.23 \, ^{\circ}\text{C}$

2.9 Design RCD Snubber

In a flyback topology, a snubber circuit is required to clamp the voltage caused by the leakage inductance, which is present in all transformers. Leakage inductance is highly dependent on the transformer construction, so care should be taken to keep it less than 2% of primary inductance. When no leakage inductance is known, a leakage inductance value of $37\mu H$ (2% of 1.82mH) can be used as a starting point to calculate the snubber's resistor, capacitor, and diode values.

D_{SN} is selected as 1N4007GP (1000V, 1A axial lead).

Energy stored in the leakage inductance Le:

$$W1 = 0.5L_e \bullet I_{\text{rms}}^2 = 0.5 \bullet 37 \mu H \bullet 0.213^2 \approx 0.84 \mu J$$

Average power transferred from W1 to the snubber:

$$P1 = W1 \bullet f_{S} = 0.84 \mu J \bullet 67 kHz \approx 56 mW$$

$$V_{\text{max}} = V_{\text{DC_bulk_max}} + V_{\text{ro}} + V_{\text{spike}}$$

$$V_{\text{max}} = 191 V + 127 V + 50 V = 368 V$$

•
$$V_{ro} = 127V$$

Limiting the voltage to 368V.

$$\begin{split} V_{\rm SN} &= 368 V - 127 V = 241 V \\ R_{\rm SN} &= \frac{{V_{\rm SN}}^2}{P1} = \frac{241 V^2}{0.056 W} \approx 1 M \Omega \\ C_{\rm SN} &= \frac{T_{\rm S}}{R_{\rm SN}} = \frac{15 \, \mu s}{1 M \Omega} = 0.015 n F \\ C_{\rm SN} \gg \frac{T_{\rm S}}{R_{\rm SN}} &= \frac{1}{f_{\rm S} \bullet R_{\rm SN}} = \frac{1}{67 k H z \bullet 1 M \Omega} \approx 15 p F \end{split}$$

For margin, C_{SN} =1nF is selected. This value is a starting point, and might have to be adjusted to get the desired voltage spike suppression.

2.10 Over-Voltage Protection & Zero-Crossing Detection

The application schematic in **Figure 2 IX9907 Application Circuit** shows that the voltage from the auxiliary winding is connected to the zero crossing pin (ZCV), via RC network resistor R_{ZCV1} , R_{ZCV2} and C_{ZCV} . The circuit provides a delay so that switch-on can occur at the voltage valley, thus enhancing efficiency. The line voltage is sensed indirectly through the current in R_{ZCV1} .

The current equation is given by:

$$I_{\text{ZCV}} = \frac{V_{\text{DC bulk min}} \bullet N_{\text{A}}}{R_{\text{ZCV1}} \bullet N_{\text{P}}}$$

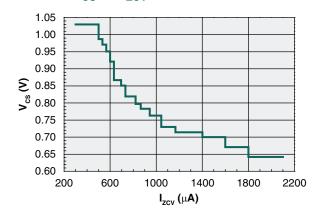
Solving for R_{ZCV1}:

$$R_{\rm ZCV1} = \frac{V_{\rm DC_bulk_min}}{I_{\rm ZCV}} \bullet \frac{N_{\rm A}}{N_{\rm P}}$$

From the V_{CS} vs. I_{ZCV} graph below we select $I_{ZCV} = 1000 \mu A$ as the recommended current. For N_A/N_P value, see 3.2 Determine Transformer Turns Ratio.

$$R_{\text{ZCV1}} = \frac{127.3 V}{0.001.4} \bullet 0.1468 \approx 18.7 k\Omega$$

Figure 3 V_{CS} vs. I_{ZCV}



The over voltage detection can be programmed by R_{ZCV2} resistor. The output, $V_{LEDstring}$, is 20V, so select an over voltage protection of 35V, and from the datasheet, $V_{ZCVOVP} = 3.7V$

$$R_{\text{ZCV2}} = \frac{R_{\text{ZCV1}} \bullet V_{\text{ZCVOVP}}}{V_{\text{oovnth}} - V_{\text{ZCVOVP}}}$$

$$R_{\rm ZCV2} = \frac{18.7k\Omega \bullet 3.7V}{35V - 3.7V} \approx 2.210k\Omega$$



The delay capacitor can be approximated:

$$\begin{split} C_{\text{ZCV}} &= t_{\text{RC}} \bullet \frac{R_{\text{ZCV1}} + R_{\text{ZCV2}}}{R_{\text{ZC1}} \bullet R_{\text{ZC2}}} \\ C_{\text{ZCV}} &= 1 \, \mu s \, \bullet \left(\frac{18.7 k\Omega + 2.210 k\Omega}{18.7 k\Omega \bullet 2.210 k\Omega} \right) \approx 519 pF \end{split}$$

Where $t_{BC} = 1 \mu s$ (see IX9907 data sheet).

2.11 Primary Peak Current Control

The value of the current sense resistor (R_{CS}) can be selected by using the following equation:

$$R_{\rm CS} = \frac{V_{\rm CSmax}}{I_{\rm pri_peak}}$$

$$R_{\rm CS} = \frac{0.75 \, V}{0.522 A_{\rm peak}} \approx 1.44 \, \Omega$$

The R_{IN1} and R_{IN2} resistor values can be selected to scale the input voltage at the V_R pin. The R_{IN1} resistor is selected with consideration of losses and high power factor correction. In this example we select $R_{IN1} = 1046 k\Omega$. Select two $523 k\Omega$ standard value resistors in the 0805 package.

The R_{IN2} value can be calculated:

$$R_{\rm IN2} = \frac{R_{\rm IN1} \bullet R_{\rm CS} \bullet G_{\rm PWM} \bullet I_{\rm pri_peak}}{V_{\rm DC\ bulk\ min} - (G_{\rm PWM} \bullet I_{\rm pri\ peak} \bullet R_{\rm CS})}$$

 R_{IN2} = 18.8k Ω . Select a standard value resistor, 18.7k Ω , where:

PWM-Op gain, $G_{PWM} = 3$ (from the IX9907 data sheet).

Select the C_{VR} capacitor to be 2.2nF and the D_{VR} diode can be BAS16, 100V, or equivalent.

2.12 Design of V_{CC} Start-Up Power Supply and Component Selection for Auxiliary Power Supply

The start-up power supply can be implemented using depletion mode power DMOS, zener diode, resistor and blocking diode. In this case Q1, CPC5603C, is selected with a 20V Zener diode, MMSZ5250B, and R1, $402k\Omega$ resistor, which provides bias current for Z1. The blocking diode for V_{CC} start-up and auxiliary D_{VCC1} , D_{VCC2} is selected BAS16, 100V diode or equivalent. V_{CC1} capacitor $10\mu\text{F}$ and small bypass $0.1\mu\text{F}$ will be adequate; however, if there is dimming requirement a capacitor of $22\mu\text{F}$ should be selected.

2.13 Output Schottky Diode and Capacitor

The Schottky output diode is exposed to large currents when the converter is operated in critical conduction mode, CCM. Care should be taken to ensure adequate margins for the voltage and current ratings.

The required blocking voltage for D_{OUT}:

$$V_{\text{d_out}} > \left(\sqrt{2} \bullet 135 V_{\text{AC_max}} \bullet \frac{N_{\text{S}}}{N_{\text{P}}}\right) + V_{\text{LEDstring}}$$

 $V_{\text{d_out}} > (191 V \bullet 0.1626) + 20 V \approx 51 V$

See **3.2 Determine Transformer Turns Ratio** for the N_S/N_P ratio. Select a Schottky diode with a 150V rating.

The output diode is exposed to large peak currents.

$$I_{\text{d_rms}} = I_{\text{rms}} \bullet \sqrt{\frac{1 - D_{\text{max}}}{D_{\text{max}}}} \bullet \frac{V_{\text{ro}}}{V_{\text{LEDstring}} + V_{\text{f}}}$$
$$I_{\text{d_rms}} \approx 0.213 A_{\text{rms}} \bullet 1 \bullet 6.135 = 1.31 A_{\text{rms}}$$

See **2.4 Primary Inductor Currents Calculation**. For adequate margin, select a $3A_{rms}$ diode, such as STPS3150V.

The output capacitor can be selected based on the desired output voltage ripple. The dynamic resistance of the LED driven at a given current should also be considered, as this will determine the current ripple through the LED string.

In this application, the LEDs have a dynamic resistance of 1Ω measured at 500mA operating current. 6 LEDs result in 6Ω of dynamic resistance.

$$V_{
m out_rip} = I_{
m LED_max} \bullet R_{
m dynamic}$$

$$V_{
m out_rip} = 500 mA \bullet 6\Omega = 3 V_{
m PP}$$

The output capacitor can be approximated:

$$C_{\text{out}} = \frac{2 \cdot I_{\text{LED max}}}{V_{\text{out_rip}} \cdot 2 \cdot \pi \cdot 120Hz}$$

$$C_{\text{out}} = \frac{2 \cdot 500mA}{3V_{\text{PP}} \cdot 2 \cdot \pi \cdot 120Hz} \approx 442 \,\mu F$$

Select two $220\mu F$ capacitors, EEV-FM1V221L. These capacitors are rated at $105^{\circ}C$ for 4000 hours with 100kHz frequency.



3 Transformer Design

A step-by-step guide for transformer design will be presented here. Primary inductance and turns ratio will be calculated, and the appropriate wire size will be selected. The transformer core and coil former will be selected to support design power requirements, and a general guideline will be presented for transformer construction to achieve the best efficiency, and avoiding transformer saturation at higher temperatures.

3.1 Primary Inductance Calculation

$$L_{\text{pri}} = \frac{\left(V_{\text{DC_bulk_min}} \bullet \eta \bullet D_{\text{max}}\right)^2}{2 \bullet P_{\text{IN}} \bullet F_{\text{S}}}$$

$$L_{\text{pri}} = \frac{(127.3 \, V \bullet 0.85 \bullet 0.5)^2}{2 \bullet 12 \, W \bullet 67 kHz} \approx 1.82 \, mH$$

3.2 Determine Transformer Turns Ratio

$$\frac{V_{\text{LEDstring}}}{V_{\text{IN}}} = \frac{N_{\text{S}}}{N_{\text{P}}} \bullet \frac{D_{\text{max}}}{1 - D_{\text{max}}}$$

Secondary to primary turns ratio

$$\frac{N_{\rm S}}{N_{\rm P}} = \frac{V_{\rm LEDstring} + V_{\rm f}}{\sqrt{2} \bullet V_{\rm AC \ min}} \bullet \frac{1 - D_{max}}{D_{max}}$$

Where V_f is the voltage drop of the output diode.

$$\frac{N_{\rm S}}{N_{\rm p}} = \frac{20V + 0.7V}{\sqrt{2} \cdot 90V} \cdot \frac{1 - 0.5}{0.5} \approx 0.1626$$

The auxiliary to primary turns ratio:

$$\frac{N_{\rm A}}{N_{\rm P}} = \frac{V_{\rm aux_max} + V_{\rm f}}{\sqrt{2} \bullet V_{\rm AC_min}} \bullet \frac{1 - D_{\rm max}}{D_{\rm max}}$$

$$\frac{N_{\rm A}}{N_{\rm P}} = \frac{18V + 0.7V}{\sqrt{2} \cdot 90V} \cdot \frac{1 - 0.5}{0.5} \approx 0.1469$$

The auxiliary to secondary turns ratio:

$$\frac{N_{\rm A}}{N_{\rm S}} = \frac{V_{\rm aux~max} + V_{\rm f}}{V_{\rm LEDstring} + V_{\rm f}}$$
$$\frac{N_{\rm A}}{N_{\rm S}} = \frac{18V + 0.7V}{20V + 0.7V} \approx 0.9034$$

3.3 Determine Primary Winding Wire Size

The first step is to find the RMS current through the primary winding, which can be approximated with the following equation:

$$I_{\rm rms} = I_{\rm pri_peak} \bullet \sqrt{\frac{D_{\rm max}}{3}}$$

$$I_{\rm rms} = 0.522 A_{\rm peak} \bullet \sqrt{\frac{0.5}{3}} \approx 0.213 A_{\rm rms}$$

The primary winding wire area can be calculated:

$$A_{\text{w_pri}} = \frac{I_{\text{rms}}}{J_{\text{max}}} \approx \frac{0.213 A_{\text{rms}}}{6 A / mm^2} = 0.0355 mm^2$$

Where J_{max} is the current density of a wire, which is stated by the wire manufacturer to be 6A/mm².

The diameter is determined by the following formula:

Wire Diameter =
$$2 \bullet \sqrt{\frac{A_{\rm W}}{\pi}} = \sqrt{\frac{A_{\rm W}}{\pi/4}} \approx \sqrt{\frac{A_{\rm W}}{0.7854}}$$

$$D_{\rm w_pri} \approx \sqrt{\frac{0.0355mm^2}{0.7854}} \approx 0.213mm$$

From **Table 1: Wire Gauge Table** this is converted to 32 AWG.

3.4 Determine Secondary Winding Wire Size

The peak current of secondary winding can be determined by the following formula:

$$I_{\text{sec_peak}} = \frac{2 \bullet I_{\text{LEDmax}}}{1 - D_{\text{max}}} = \frac{2 \bullet 0.5A}{1 - 0.5} = 2A_{\text{peak}}$$

The RMS current can be found:

$$I_{\text{sec_rms}} = I_{\text{sec_peak}} \bullet \sqrt{\frac{1 - D_{\text{max}}}{3}} \approx 2A_{\text{peak}} \bullet 0.408 \approx 0.817A_{\text{rms}}$$

Calculate the secondary wire size:

$$A_{\text{w_sec}} = \frac{I_{\text{sec rms}}}{6} \approx \frac{0.817A_{\text{rms}}}{6} \approx 0.136mm^2$$

Calculate the secondary wire diameter:

$$D_{\text{w_sec}} \approx \sqrt{\frac{0.136mm^2}{0.7854}} \approx 0.416mm$$

Convert using Table 1: Wire Gauge Table to AWG 26.



3.5 Determine Auxiliary Winding Wire Size

The maximum current in the auxiliary winding can be calculated using:

$$I_{\text{aux_peak}} = \frac{2 \bullet I_{\text{aux_max}}}{1 - D_{\text{max}}} = \frac{2 \bullet 30 mA}{1 - 0.5} = 0.12 A_{\text{peak}}$$

The RMS current can be calculated:

$$I_{\mathrm{aux_rms}} = I_{\mathrm{aux_peak}} \bullet \sqrt{\frac{1 - D_{\mathrm{max}}}{3}} \approx 0.049 A_{\mathrm{rms}}$$

The required wire area is given:

$$A_{\text{w_aux}} = \frac{I_{\text{aux_rms}}}{6} \approx 0.0082 mm^2$$

Wire diameter:

$$D_{\text{w_aux}} \approx \sqrt{\frac{0.0082mm^2}{0.7854}} \approx 0.102mm$$

Convert using Table 1: Wire Gauge Table to AWG 38.

Table 1: Wire Gauge Table

American Wire Gauge (AWG)	Equivalent Wire Diameter (mm)	Equivalent Wire Area (mm ²)
40	0.0799	0.00501
39	0.0897	0.00632
38	0.101	0.00797
37	0.113	0.01
36	0.127	0.0127
35	0.143	0.016
34	0.16	0.0201
33	0.18	0.0254
32	0.202	0.032
31	0.227	0.0404
30	0.255	0.051
29	0.286	0.0642
28	0.321	0.081
27	0.361	0.102
26	0.405	0.129
25	0.455	0.162
24	0.511	0.205
23	0.573	0.258
22	0.644	0.326
21	0.723	0.41
20	0.812	0.518
19	0.912	0.653
18	1.024	0.823
17	1.15	1.038
16	1.29	1.31
15	1.45	1.65
14	1.63	2.08
13	1.83	2.62
12	2.05	3.31
11	2.3	4.17
10	2.59	5.26
9	2.91	6.63
8	3.26	8.37
7	3.67	10.55
6	4.11	13.3
5	4.62	16.75
4	5.19	21.15
3	5.83	26.67
2	6.54	33.62
1	7.35	42.4



3.6 Select Inductor Core & Calculate Turns

The Ferroxcube catalog lists the following specifications for the E20/10/5 core set.

- $\Sigma(I_{o}/A_{o})$ core factor = 1.37 mm⁻¹
- V_e effective volume = 1340 mm³
- I_e effective length = 42.8 mm
- A_e effective area = 31.2 mm²

The air gap, G, of 0.3mm may be used for initial calculation. The effective permeability of the core can be calculated:

$$\mu_{e} = \frac{\mu_{i}}{1 + \frac{G \bullet \mu_{i}}{l_{e}}}$$

Where μ_i = 2000 (for N27 material or 1P2400) and G=0.3mm.

$$\mu_{\rm e} = \frac{2000}{1 + \frac{0.3mm \cdot 2000}{42.8mm}} \approx 133$$

Calculate Inductance Factor A_I for this core:

$$A_{L} = \frac{\mu_{o} \cdot \mu_{e}}{\sum \frac{l_{e}}{A_{e}}}$$

$$A_{L} \approx \frac{4\pi \cdot 10^{-7} \text{H} \cdot 133}{m \cdot turn^{2}} \cdot \frac{10^{-3}}{1.37} \approx \frac{121.99 \text{nH}}{turn^{2}}$$

Where μ_0 = vacuum permeability = 4 π *10⁻⁷H/m.

Calculate number of turns for the primary winding:

$$N_{\rm pri} = \sqrt{\frac{L_{\rm pri}}{A_{\rm I}}} \approx \sqrt{\frac{1.82mH}{121.99nH}} \bullet turn^2 \approx 122 \text{ turns}$$

Calculate the maximum flux density:

$$B_{\text{max}} = N_{\text{pri}} \bullet I_{\text{pri_peak}} \bullet \frac{A_{\text{L}}}{A_{\text{e}}}$$

$$\frac{122 turn \bullet 0.522 A_{\text{peak}} \bullet \frac{121.99 nH}{turn^2}}{31.2 mm^2} \approx 0.248 T$$

Note that B_{max} for 1P2400, MnZn ferrite, is 0.36T at 100°C.

Calculate number of turns for secondary and auxiliary windings:

$$N_{\rm sec} = N_{\rm pri} \bullet \frac{N_{\rm S}}{N_{\rm P}} \approx 122 \, turns \bullet 0.1626 \approx 20 \, {\rm turns}$$

$$N_{\rm aux} = N_{\rm pri} \bullet \frac{N_{\rm A}}{N_{\rm P}} \approx 122 \, turns \bullet 0.1468 \approx 18 \, {\rm turns}$$

Calculate to make sure windings will fit the winding area of coil former. The Ferroxcube data sheet provides information for 8-pin coil former and specifies winding area of 27mm².

The fill factor has to be taken into consideration as this will affect winding area for this coil former. In general we can use fill factor Ku of 0.3 to 0.7 as a starting point. In this case 0.3 will be selected.

Total Winding Area =
$$\frac{1}{Ku} \bullet [(N_{\text{pri}} \bullet A_{\text{w_pri}}) + (N_{\text{sec}} \bullet A_{\text{w_sec}}) + (N_{\text{aux}} \bullet A_{\text{w_aux}})] \le 27mm^2$$

Total Winding Area $\approx 3.33 \bullet [(122 \bullet 0.0355mm^2) + (20 \bullet 0.136mm^2) + (18 \bullet 0.0081mm^2)] \le 27mm^2$

Total Winding Area $\approx 24mm^2 \le 27mm^2$

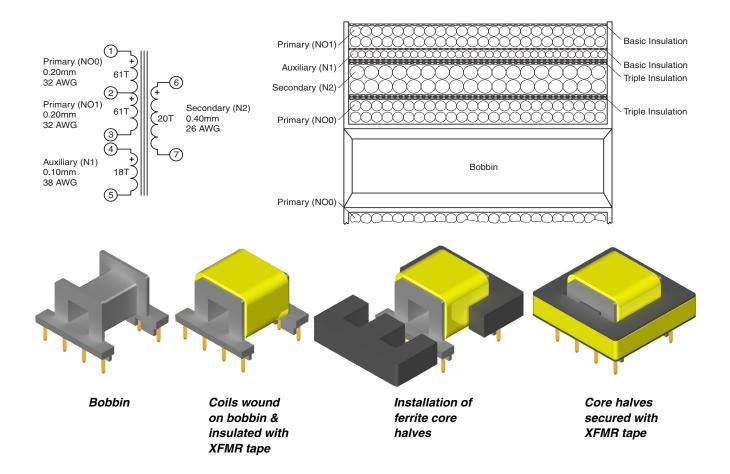
Note: The total winding area should not be exceeded due to the selection of approximate standard wire gauges from **Table 1: Wire Gauge Table**.



3.7 Flyback Transformer Construction

The transformer block diagram shows the primary split into two sections of 61 turns each. The secondary and auxiliary are wound between the two primary windings. The transformer stack-up has three layers of insulation tape between the primary and the secondary side. This method enables the transformer to pass the safety standard for electrical strength requirement. In the

previous step, we selected an air gap of 0.3mm as our starting point. The air gap is critical because it allows the transformer to extend its maximum saturation; however, if the gap is larger, then it could contribute to higher leakage inductance. In this example, it is possible to achieve a leakage inductance less than 2% of primary.



For additional information please visit our website at: www.ixysic.com

IXYS Integrated Circuits Division makes no representations or warranties with respect to the accuracy or completeness of the contents of this publication and reserves the right to make changes to specifications and product descriptions at any time without notice. Neither circuit patent licenses nor indemnity are expressed or implied. Except as set forth in IXYS Integrated Circuits Division's Standard Terms and Conditions of Sale, IXYS Integrated Circuits Division assumes no liability whatsoever, and disclaims any express or implied warranty, relating to its products including, but not limited to, the implied warranty of merchantability, fitness for a particular purpose, or infringement of any intellectual property right.

The products described in this document are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or where malfunction of IXYS Integrated Circuits Division's product may result in direct physical harm, injury, or death to a person or severe property or environmental damage. IXYS Integrated Circuits Division reserves the right to discontinue or make changes to its products at any time without notice.

Specification: AN-305-R02 ©Copyright 2016, IXYS Integrated Circuits Division All rights reserved. Printed in USA. 7/14/2016