

Date:- 2nd Dec 2010

Data Sheet Issue:- 1

Prospective Data

Anode Shorted Gate Turn-Off Thyristor Types G2500HF250

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{DRM}	Repetitive peak off-state voltage, (note 1)	2500	V
V_{RSM}	Non-repetitive peak off-state voltage, (note 1)	2500	V
$V_{DC ext{-link}}$	Maximum continuos DC-link voltage	1250	V
V_{RRM}	Repetitive peak reverse voltage	18	V
V_{RSM}	Non-repetitive peak reverse voltage	18	V

	RATINGS	MAXIMUM LIMITS	UNITS
I_{TGQ}	Peak turn-off current, (note 2)	2500	Α
Ls	Snubber loop inductance, I _{TM} =I _{TGQ} , (note 2)	200	nΗ
$I_{T(AV)M}$	Mean on-state current, T _{sink} =55°C (note 3)	1085	Α
$I_{T(RMS)}$	Nominal RMS on-state current, 25°C (note 3)	2133	Α
I _{TSM}	Peak non-repetitive surge current t _p =10ms, (Note 4)	16	kA
I _{TSM2}	Peak non-repetitive surge current t _p =2ms, (Note 4)	21	kA
I ² t	I ² t capacity for fusing t _p =10ms	1.28×10 ⁶	A^2s
di/dt _{cr}	Critical rate of rise of on-state current, (note 5)	500	A/µs
P _{FGM}	Peak forward gate power	120	W
P_{RGM}	Peak reverse gate power	12	kW
I _{FGM}	Peak forward gate current	60	Α
V_{RGM}	Peak reverse gate voltage (note 6).	18	V
T _{j op}	Operating temperature range	-40 to +125	°C
T_{stg}	Storage temperature range	-40 to +125	°C

Notes:-

- 1) V_{GK}=-2Volts.
- 2) T_j =125°C, V_D =1250V, $V_{DM} \le$ 2500V di_{GQ}/dt =30A/ μ s, I_{TGQ} =2500A and C_S =6 μ F.
- 3) Double-side cooled, single phase; 50Hz, 180° half-sinewave.
- 4) T_{j(initial)}=125°C, single phase, 180° sinewave, re-applied voltage V_D=V_R≤10V.
- 5) I_T=3000A repetitive, I_{GM}=25A, di_{GM}/dt=20A/µs. For di/dt>500A/µs please consult the factory.
- 6) May exceed this value during turn-off avalanche period.



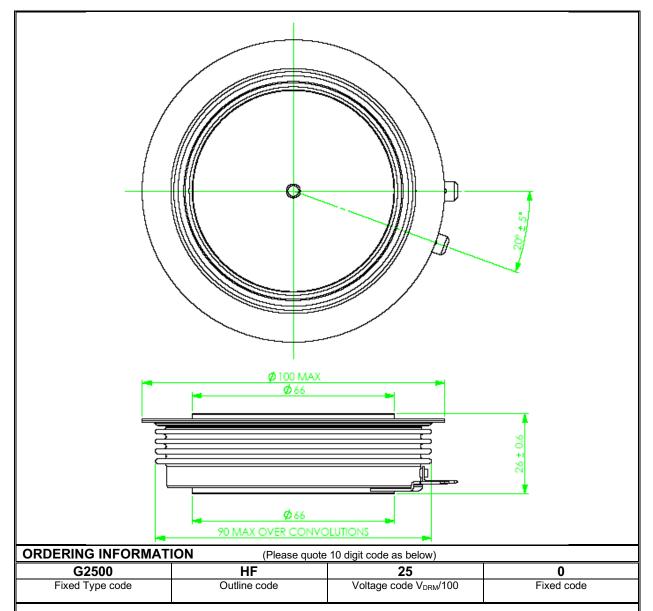
Characteristics

	Parameter	MIN	TYP	MAX	TEST CONDITIONS	UNITS
V _{TM}	Maximum peak on-state voltage	-	-	3.1	I _G =5A, I _T =2500A	V
IL	Latching current	-	40	-	T _j =25°C	Α
I _H	Holding current.	-	40	-	T _j =25°C	Α
dv/dt _{cr}	Critical rate of rise of off-state voltage	1000	-	-	V _D =3000V, V _{GR} =-2V	V/µs
I _{DRM}	Peak off state current	-	-	60	Rated V _{DRM} , V _{GR} =-2V	mA
I _{RRM}	Peak reverse current	-	-	20	V _{RR} =18V	mA
I _{GKM}	Peak negative gate leakage current	-	-	20	V _{GR} =-18V	mA
V_{GT}	Gate trigger voltage	-	1.0	-	T_j =-40°C T_j =25°C V_D =25V, R_L =25m Ω T_j =125°C	V
		-	0.8	1.0		V
		-	0.6	-		V
I _{GT}	Gate trigger current	-	8	-	T_j =-40°C T_j =25°C V_D =25V, R_L =25m Ω	А
		-	-	5		А
		50	-	1	T _j =125°C	mA
t _d	Delay time	-	0.7	2		μs
t _{gt}	Turn-on time	-	3	5	V_D =1250V, I_{TGQ} =2500A, di_T/dt =200A/ μ s, I_{GM} =30A, di_G/dt =20A/ μ s, C_S =6 μ F, Rs=5 Ω	μs
E _{on}	Turn-on energy	-	-	0.5	16μ-50Λ, αιζιατ-20Λ/μο, 05-0μι , 13-552	J
t _f	Fall time	-	2	-		μs
ts	Storage time	-	-	26		μs
t_{gq}	Turn-off time	-	-	30		μs
I_{GQM}	Peak turn-off gate current	-	680	-	V_{DM} =2500V, I_{TGQ} =2500A, di_{GQ}/dt =30A/ μ s, V_{GR} =-16V, C_{S} =6 μ F	А
Q_{GQ}	Turn-off gate charge	-	9	-	, , , , , , , , , , , , , , , , , , ,	mC
t _{tail}	Tail time	-	10	-		μs
E _{off}	Turn-off energy	-	-	3.2		J
R_{thJK}		-	20	-	Double side cooled	K/kW
	Thermal resistance junction to sink	-	44	-	Cathode side cooled	K/kW
		-	37	-	Anode side cooled	K/kW
F	Mounting force	21	-	26	(see note 2)	kN
W_t	Weight	_	0.8	_		kg

Notes:-

- Unless otherwise indicated T_i=125°C. For other clamping forces, consult factory.

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