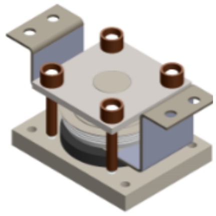
IXYS UK WESTCODE



Date: 14th Dec, 2017

Data Sheet Issue: 1

Phase Control Thyristor Module
Types N4340TJ180MBR to N4340TJ220MBR

Absolute Maximum Ratings

V_{RRM} V_{DRM} [V]	Type
1800	N4340TJ180MBR
2200	N4340TJ220MBR

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{DRM}	Repetitive peak off-state voltage 1)	1800-2200	V
V_{DSM}	Non-repetitive peak off-state voltage 1)	1800-2200	V
V_{RRM}	Repetitive peak reverse voltage 1)	1800-2200	V
V_{RSM}	Non-repetitive peak reverse voltage 1)	1900-2300	V

	OTHER RATINGS		MAXIMUM LIMITS	UNITS
$I_{T(AV)M}$	Maximum average on-state current, T_c=55°C 2)		1715	A
$I_{T(AV)M}$	Maximum average on-state current, T_c=85°C 2)		1110	A
$I_{T(RMS)M}$	Nominal RMS on-state current, T_c=25°C 2)		3500	A
$I_{T(d.c.)}$	D.C. on-state current, T_c=25°C		2230	A
I_{TSM}	Peak non-repetitive surge t_p=10ms, V_{rm}=60%V_{RRM} 3)		55	kA
I_{TSM2}	Peak non-repetitive surge t_p=10ms, $V_{rm}\leq$10V 3)		60	kA
I^2t	I^2t capacity for fusing t_p=10ms, V_{rm}=60%V_{RRM} 3)		15.1×10^6	A^2s
I^2t	I^2t capacity for fusing t_p=10ms, $V_{rm}\leq$10V 3)		18.3×10^6	A^2s
$(di/dt)_{cr}$	Critical rate of rise of on-state current 4)	(continuous, 50Hz)	100	A/µs
		(repetitive, 50Hz, 60s)	200	
		(non-repetitive)	400	
V_{RGM}	Peak reverse gate voltage		5	V
$P_{G(AV)}$	Mean forward gate power		5	W
P_{GM}	Peak forward gate power		30	W
V_{ISOL}	Isolation Voltage 5)		3000	V
$T_{vj\ op}$	Operating temperature range		-40 to +125	°C
T_{stg}	Storage temperature range		-40 to +150	°C

Notes:
1) De-rating factor of 0.13% per °C is applicable for T_{vj} below 25°C.
2) Single phase; 50 Hz, 180° half-sinewave.
3) Half-sinewave, 125°C T_{vj} initial.
4) V_D = 67% V_{DRM}, I_{TM}=2000A, I_{FG} = 2 A, $t_r \leq$ 0.5µs, T_C = 125°C.
5) AC RMS voltage, 50 Hz, 1min test

Characteristics

	PARAMETER	MIN.	TYP.	MAX.	TEST CONDITIONS [1)]	UNITS
V_{TM}	Maximum peak on-state voltage	-	-	1.35	I_{TM}=4300A	V
V_{TM}	Maximum peak on-state voltage	-	-	2.12	I_{TM}=13000A	V
V_{T0}	Threshold voltage	-	-	0.886		V
r_T	Slope resistance	-	-	0.105		mΩ
$(dv/dt)_{cr}$	Critical rate of rise of off-state voltage	1000	-	-	V_D = 80% V_{DRM}, linear ramp, Gate o/c	V/µs
I_{DRM}	Peak off-state current	-	-	200	Rated V_{DRM}	mA
I_{RRM}	Peak reverse current	-	-	200	Rated V_{RRM}	
V_{GT}	Gate trigger voltage	-	-	3.0	T_j=25°C	V
I_{GT}	Gate trigger current	-	-	300	V_D=10V, I_T=3A	mA
V_{GD}	Gate non-trigger voltage	-	-	0.25	Rated V_{DRM}	V
I_H	Holding current	-	-	1000	T_{vj} = 25°C	mA
t_{gd}	Gate-controlled turn-on delay time	-	0.7	1.5	V_D=67% V_{DRM}, I_T=2000A, di/dt=10A/µs,	µs
t_{gt}	Turn-on time	-	2.0	4.0	I_{FG}=2A, t_r=0.5µs, T_j=25°C	µs
Q_{rr}	Recovered Charge		6700	7400	I_{TM}=4000A, t_p=2000µs, di/dt=10A/µs, V_r=50V	µC
Q_{ra}	Recovered Charge, 50% chord		3400	-		µC
I_{rm}	Reverse recovery current		180	-		A
t_{rr}	Reverse recovery time, 50% chord		38	-		µs
R_{thJC}	Thermal resistance, junction to case	-	-	0.0306		K/W
R_{thCH}	Thermal resistance, case to heatsink	-	-	0.0035		K/W
F_1	Mounting torque (to heatsink)[2)]	16	-	23		Nm
F_2	Mounting torque (to terminals)[3)]	15	-	20		Nm
W_t	Weight	-	8.14	-		kg

Notes:
1) Unless otherwise indicated T_{vj}=125°C.
2) Heatsink use M10.
3) Terminals use M12.

Notes on Ratings and Characteristics

1.0 Voltage Grade Table

Voltage Grade	V_{DRM} V_{DSM} V_{RRM} V	V_{RSM} V	V_D V_R DC V
18	1800	1900	1080
22	2200	2300	1320

2.0 Extension of Voltage Grades

This report is applicable to other voltage grades when supply has been agreed by Sales/Production.

3.0 De-rating Factor

A blocking voltage de-rating factor of 0.13%/°C is applicable to this device for T_{vj} below 25°C.

4.0 Repetitive dv/dt

Standard dv/dt is 1000V/µs.

5.0 Snubber Components

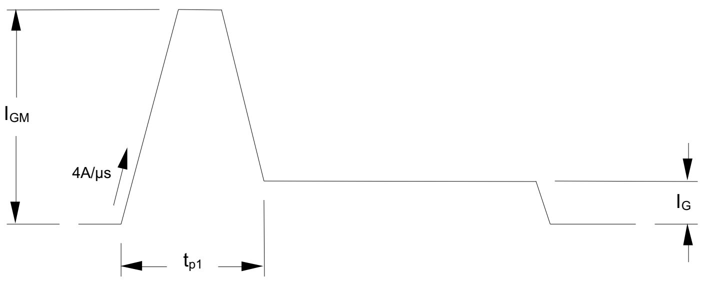
When selecting snubber components, care must be taken not to use excessively large values of snubber capacitor or excessively small values of snubber resistor. Such excessive component values may lead to device damage due to the large resultant values of snubber discharge current. If required, please consult the factory for assistance.

6.0 Rate of rise of on-state current

The maximum un-primed rate of rise of on-state current must not exceed 400A/µs at any time during turn-on on a non-repetitive basis. For repetitive performance, the on-state rate of rise of current must not exceed 200A/µs at any time during turn-on. Note that these values of rate of rise of current apply to the total device current including that from any local snubber network.

7.0 Gate Drive

The nominal requirement for a typical gate drive is illustrated below. An open circuit voltage of at least 30V is assumed. This gate drive must be applied when using the full di/dt capability of the device.



The magnitude of I_{GM} should be between five and ten times I_{GT}, which is shown on page 2. Its duration (t_{p1}) should be 20µs or sufficient to allow the anode current to reach ten times I_L, whichever is greater. Otherwise, an increase in pulse current could be needed to supply the necessary charge to trigger. The ‘back-porch’ current I_G should remain flowing for the same duration as the anode current and have a magnitude in the order of 1.5 times I_{GT}.

8.0 Computer Modelling Parameters

8.1 Thyristor dissipation calculations

$$I_{AV} = \frac{-V_{T0} + \sqrt{V_{T0}^{2} + 4 \cdot ff^{2} \cdot r_T \cdot W_{AV}}}{2 \cdot ff^{2} \cdot r_T}$$

and:

$$W_{AV} = \frac{\Delta T}{R_{th}}$$

$$\Delta T = T_{j\,max} - T_C$$

Where V_{T0} = 0.886V, r_T = 0.105mΩ.

R_{th} = Supplementary thermal impedance, see table below and

ff = Form factor, see table below.

Supplementary Thermal Impedance (Junction to Case)				
Conduction Angle	60°	120°	180°	d.c.
Square wave	0.030619	0.030616	0.030614	0.0306
Sine wave	0.030617	0.030615	0.030611	

Form Factors				
Conduction Angle	60°	120°	180°	d.c.
Square wave	2.449	1.732	1.414	1
Sine wave	2.778	1.879	1.57	

8.2 Calculating thyristor V_T using ABCD coefficients – For loss calculations

The on-state characteristic, I_T vs. V_T, is represented in two ways;

(i) the well established V_{T0} and r_T tangent used for rating purposes and

(ii) a set of constants A, B, C, D, forming the coefficients of the representative equation for V_T in terms of I_T given below:

$$V_T = A + B \cdot \ln(I_T) + C \cdot I_T + D \cdot \sqrt{I_T}$$

The constants, derived by curve fitting software, are given below for both hot and cold characteristics. The resulting values for V_T agree with the true device characteristic over a current range, which is limited to that plotted.

25°C Coefficients		125°C Coefficients	
A	1.374601	A	0.08080101
B	-0.0800689	B	-0.02039101
C	2.96792×10^{-5}	C	4.68133×10^{-5}
D	8.05124×10^{-3}	D	7.21434×10^{-3}

8.3 D.C. Thermal Impedance Calculation

$$r_t = \sum_{p=1}^{p=n} r_p \cdot \left(1 - e^{\frac{-t}{\tau_p}}\right)$$

Where p = *1* to *n*

n	=	number of terms in the series and
t	=	Duration of heating pulse in seconds.
r_t	=	Thermal resistance at time t.
r_p	=	Amplitude of p_{th} term.
τ_p	=	Time Constant of r_{th} term.

The coefficients for this device are shown in the tables below:

D.C. Junction to Case			
Term	1	2	3
r_p	0.01981779	0.009602212	0.001187377
τ_p	128.6835	15.59559	1.860866

9.0 Reverse recovery ratings

(i) Q_{ra} is based on 50% I_{RM} chord as shown in Fig. 1

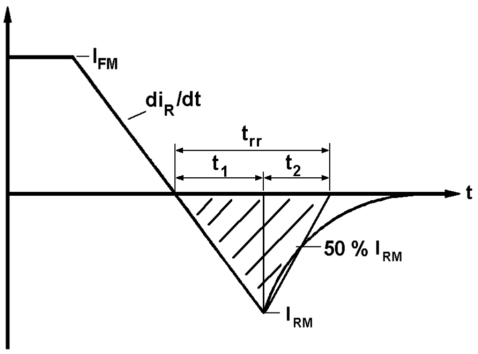


Fig. 1

(ii) Q_{rr} is based on a 150 μs integration time i.e.

$$Q_{rr} = \int_0^{150\mu s} i_{rr}.dt$$

(iii)

$$K\ Factor = \frac{t_1}{t_2}$$

Curves

Figure 1 – On-state characteristics of Limit device

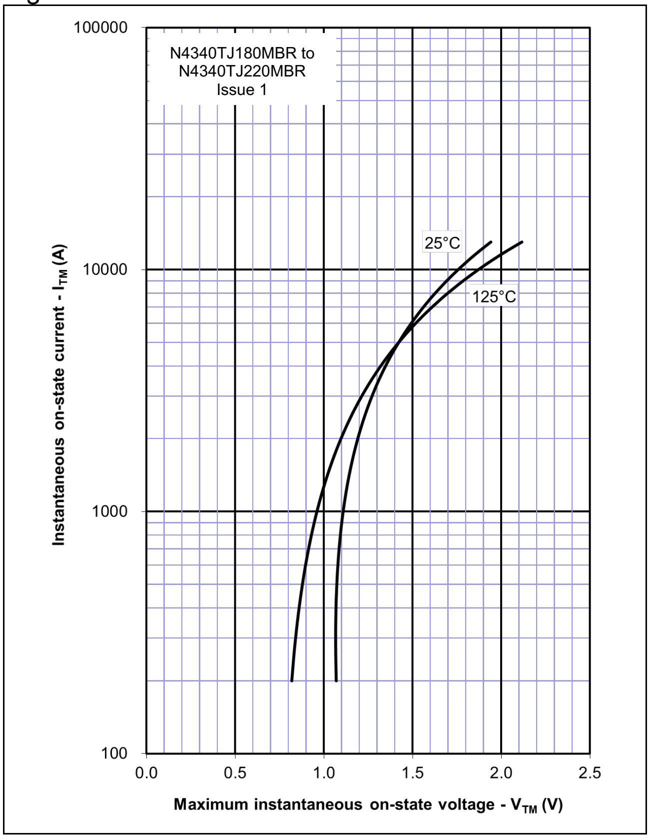


Figure 2 – Transient thermal impedance

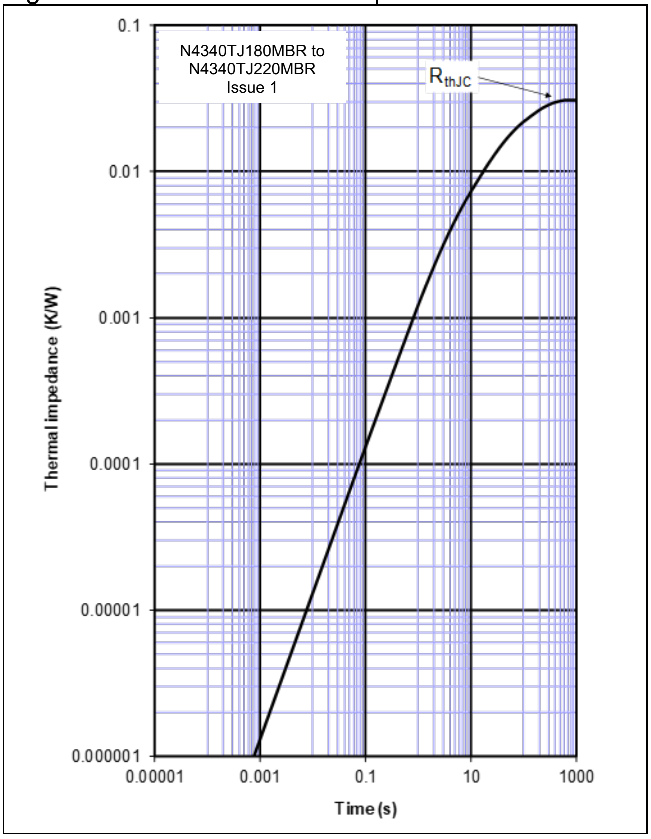


Figure 3 – Gate characteristics – Trigger limits

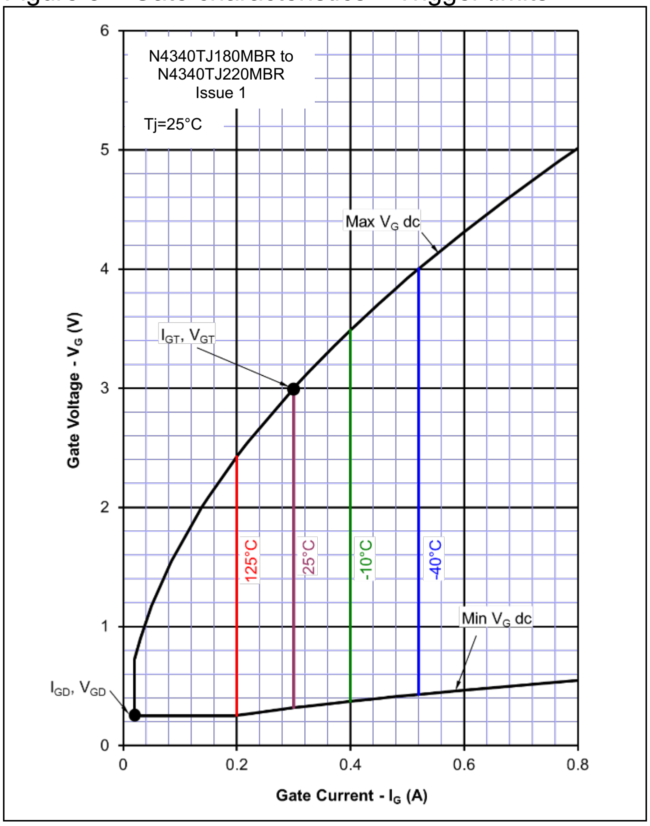


Figure 4 – Gate characteristics – Power curves

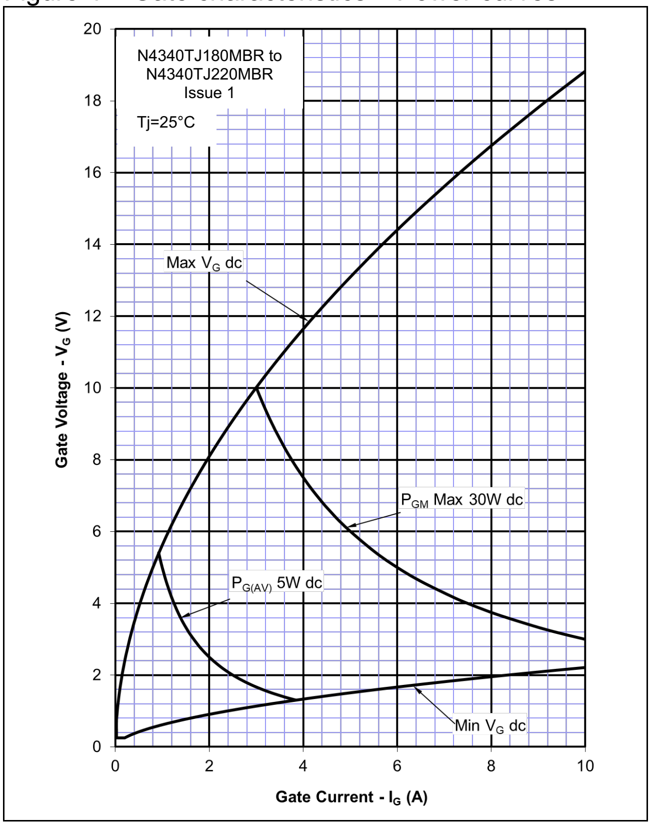


Figure 5 – On-state current vs. Power dissipation – Sine wave

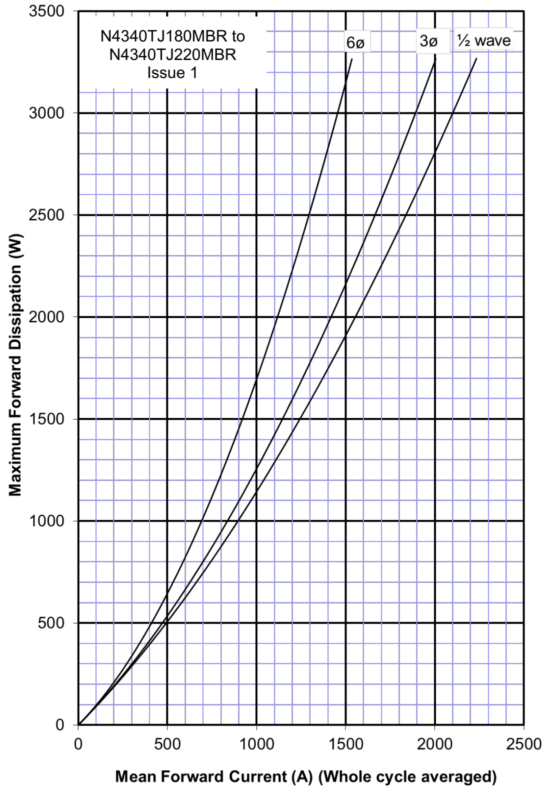


Figure 6 – On-state current vs. Heatsink temperature – Sine wave

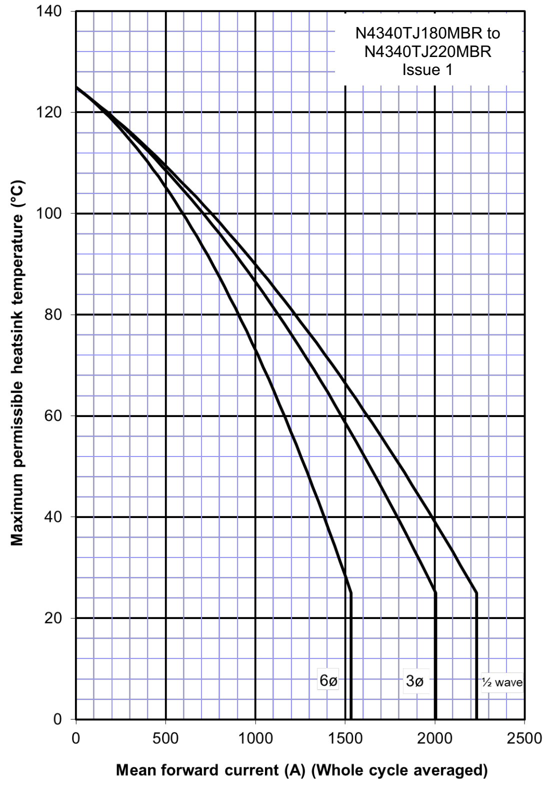


Figure 7 – On-state current vs. Power dissipation – Square wave

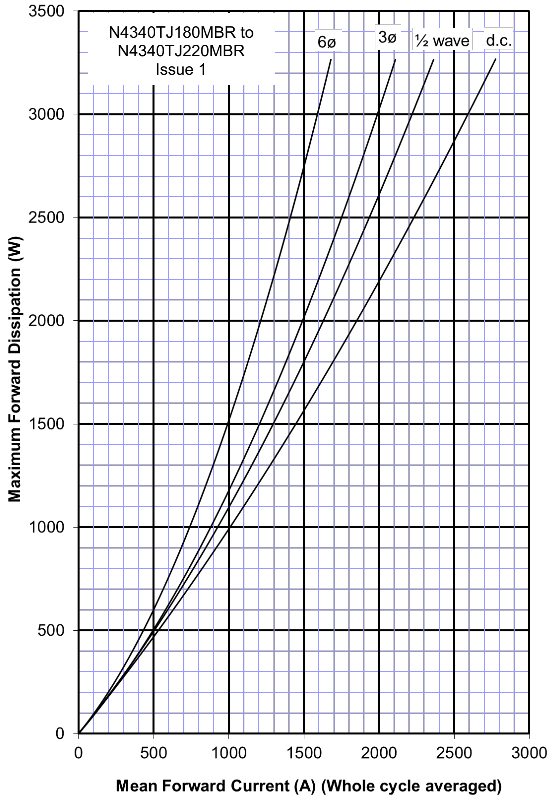


Figure 8 – On-state current vs. Heatsink temperature – Square wave

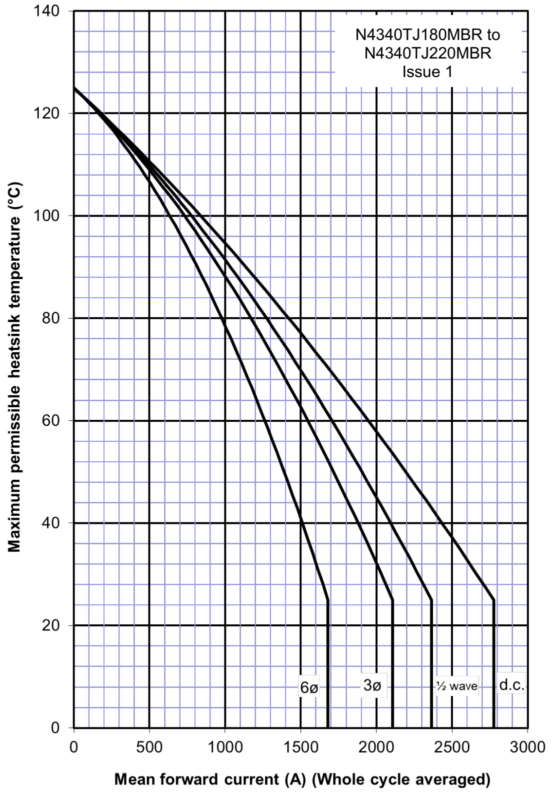
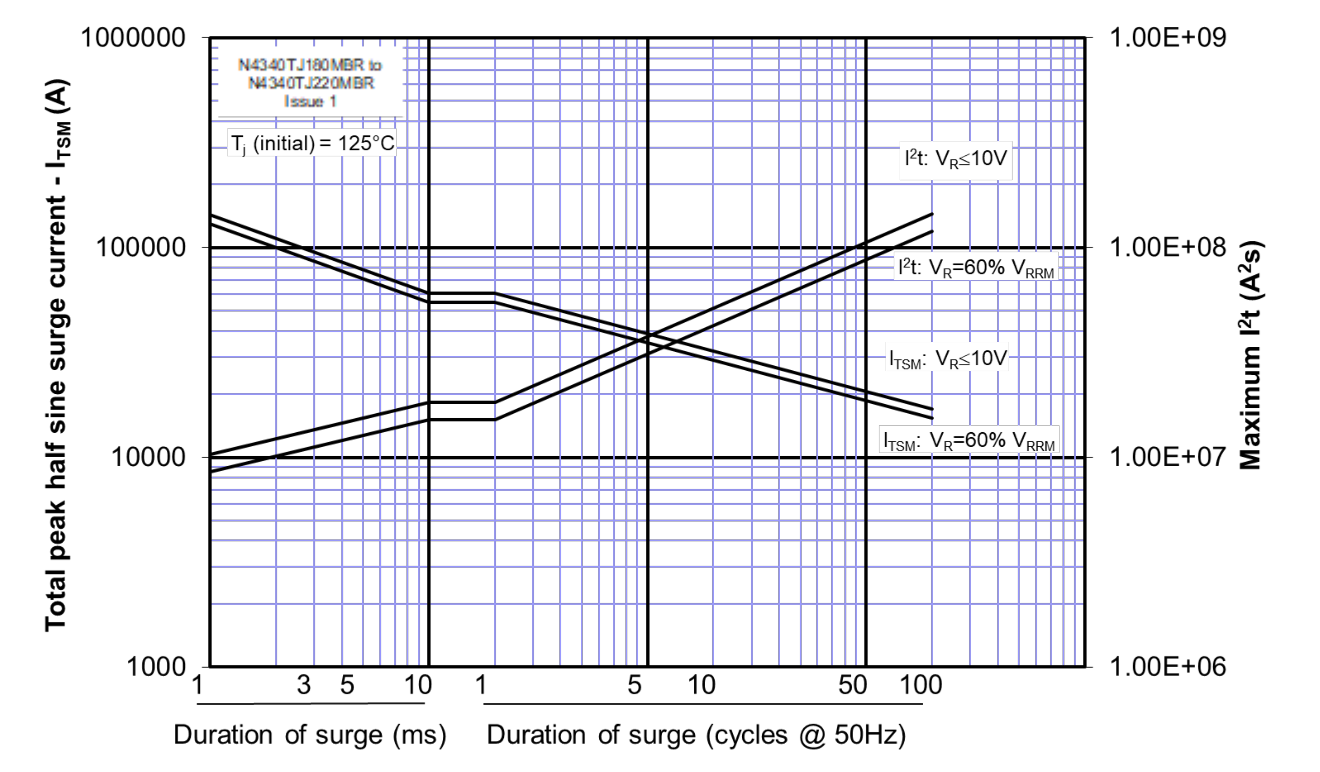
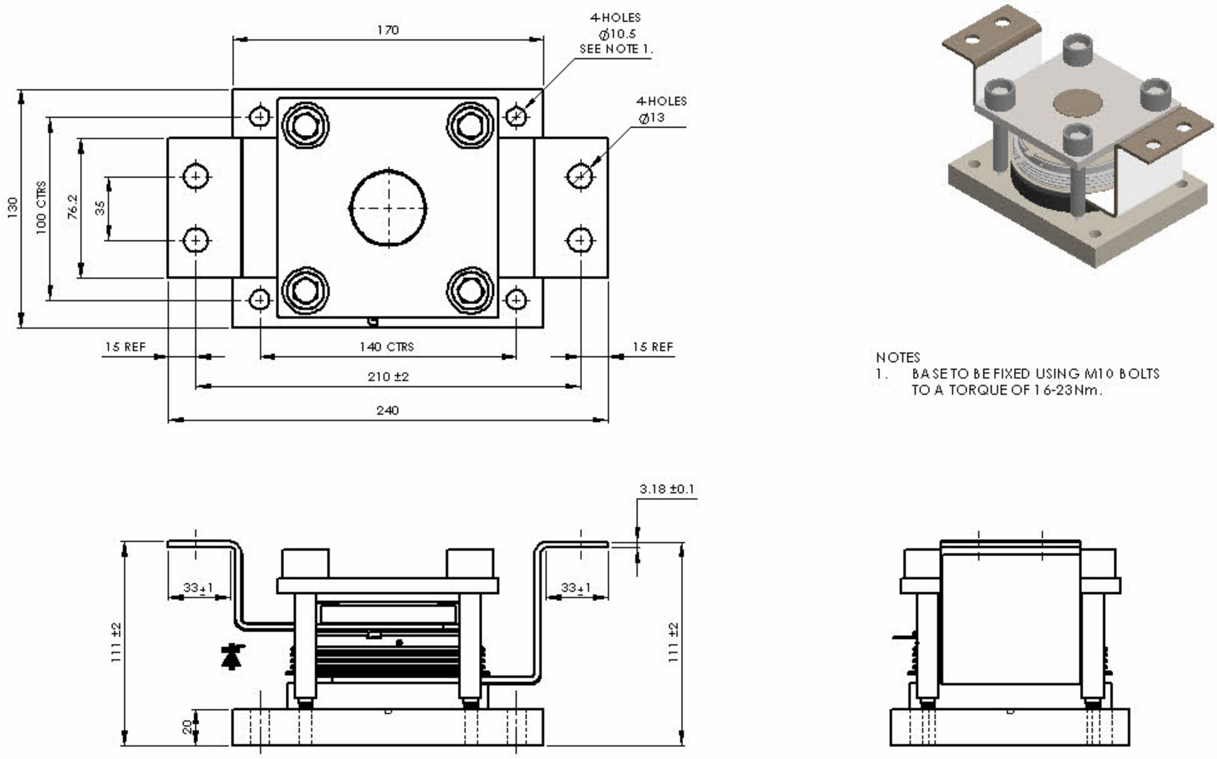


Figure 9 – Maximum surge and I^2t Ratings



Outline Drawing & Ordering Information



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ORDERING INFORMATION			(Please quote 11 digit code as below)		
N	**4340**	**TJ**	**♦♦**	**0**	**MBR**
Fixed Type Code	Nominal Current Rating	TJ=26mm clamp height	Voltage code V_{RRM}/100 18 and 22	Fixed Type Code	Fixed Configuration code

Typical order code: N4340TJ180MBR, 1800V V_{DRM}, V_{RRM} thyristor module

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