

Advance data

Insulated Gate Bi-Polar Transistor Type T1290BF65A

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{CES}	Collector – emitter voltage	6500	V
V_{CES}	Collector – emitter voltage (T_j 25°C)	6500	V
V_{CES}	Collector – emitter voltage (T_j -40°C)	6000	V
$V_{DC\ link}$	Permanent DC voltage for 100 FIT failure rate.	3600	V
V_{GES}	Peak gate – emitter voltage	±20	V

	RATINGS	MAXIMUM LIMITS	UNITS
I_C	DC collector current, IGBT	1290	A
I_{CRM}	Repetitive peak collector current, $t_p=1ms$, IGBT	2580	A
$I_{F(DC)}$	Continuous DC forward current, Diode	1290	A
I_{FRM}	Repetitive peak forward current, $t_p=1ms$, Diode	2580	A
I_{FSM}	Peak non-repetitive surge $t_p=10ms$, $V_{RM}=60\%V_{RRM}$, Diode (Note 4)	10.9	kA
I_{FSM2}	Peak non-repetitive surge $t_p=10ms$, $V_{RM}\leq 10V$, Diode (Note 4)	12	kA
P_{MAX}	Maximum power dissipation, IGBT (Note 2)	15.2	KW
$(di/dt)_{cr}$	Critical diode di/dt (note 3)	4500	A/ μs
T_j	Operating temperature range.	-40 to +125	°C
T_{stg}	Storage temperature range.	-40 to +125	°C

Notes: -

- 1) Unless otherwise indicated $T_j = 125^\circ C$.
- 2) $T_{sink} = 25^\circ C$, double side cooled.
- 3) Maximum commutation loop inductance 230nH.
- 4) Half-sinewave, 125°C T_j initial.

Characteristics

IGBT Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
$V_{CE(sat)}$	Collector – emitter saturation voltage	-	3.6	4.0	$I_C = 1290A$, $V_{GE} = 15V$, $T_j = 25^\circ C$	V
		-	4.8	5.2	$I_C = 1290A$, $V_{GE} = 15V$	V
V_{T0}	Threshold voltage	-		2.48	Current range: 430A – 1290A	V
r_T	Slope resistance	-		2.11		mΩ
$V_{GE(TH)}$	Gate threshold voltage	-	5.4	-	$V_{CE} = V_{GE}$, $I_C = 800mA$	V
I_{CES}	Collector – emitter cut-off current	-	12	45	$V_{CE} = V_{CES}$, $V_{GE} = 0V$	mA
I_{GES}	Gate leakage current	-40	-	+40	$V_{GE} = \pm 20V$	μA
C_{ies}	Input capacitance	-	225	-	$V_{CE} = 10V$, $V_{GE} = 0V$, $f = 100kHz$, $T_j = 25^\circ C$	nF
$t_{d(on)}$	Turn-on delay time	-	1.9	-	$I_C = 1290A$, $V_{CE} = 3600V$, $di/dt = 3500A/\mu s$ $V_{GE} = \pm 15V$, $L_s = 230nH$ $R_{g(ON)} = 2.4\Omega$, $R_{g(OFF)} = 7.5\Omega$, $C_{GE} = 100nF$ Integral diode used as freewheel diode (Note 3)	μs
$t_r(V)$	Rise time	-	2.8	-		μs
$Q_{g(on)}$	Turn-on gate charge	-	7.5	-		μC
E_{on}	Turn-on energy	-	9	-		J
$t_{d(off)}$	Turn-off delay time	-	4.2	-		μs
$t_f(l)$	Fall time	-	2.2	-		μs
$Q_{g(off)}$	Turn-off gate charge	-	8	-		μC
E_{off}	Turn-off energy	-	7.3	-		J
I_{SC}	Short circuit current	-	7000	-	$V_{GE} = +15V$, $V_{CC} = 3600V$, $V_{CEmax} \leq V_{CES}$, $t_p \leq 10\mu s$	A

Diode Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
V_F	Forward voltage	-	3.2	3.6	$I_F = 1290A$, $T_j = 25^\circ C$	V
		-	3.6	4.0	$I_F = 1290A$	V
V_{T0}	Threshold voltage	-	-	1.56	Current range 430A – 1290A	V
r_T	Slope resistance	-	-	1.58		mΩ
I_{rm}	Peak reverse recovery current	-	1400	-	$V_i = 3600V$, $I_F = 1290A$, $V_{GE} = -15V$, $di/dt = 3500A/\mu s$	A
Q_{rr}	Recovered charge	-	1900	-		μC
t_{rr}	Reverse recovery time, 50% chord	-	1.1	-		μs
E_r	Reverse recovery energy	-	2.8	-		J

Thermal Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
R_{thJK}	Thermal resistance junction to sink, IGBT	-	-	6.56	Double side cooled	K/kW
		-	-	9.9	Collector side cooled	K/kW
		-	-	19.5	Emitter side cooled	K/kW
R_{thJK}	Thermal resistance junction to sink, Diode	-	-	12.2	Double side cooled	K/kW
		-	-	17.6	Cathode side cooled	K/kW
		-	-	39.3	Anode side cooled	K/kW
F	Mounting force	55	-	75	Note 2	kN
W_t	Weight	-	3.2	-		g

Notes:-

- 1) Unless otherwise indicated $T_j = 125^\circ C$.
- 2) Consult application note 2008AN01 for detailed mounting requirements
- 3) C_{GE} is additional gate – emitter capacitance added to output of gate drive

Curves

Figure 1 – Typical collector-emitter saturation voltage characteristics

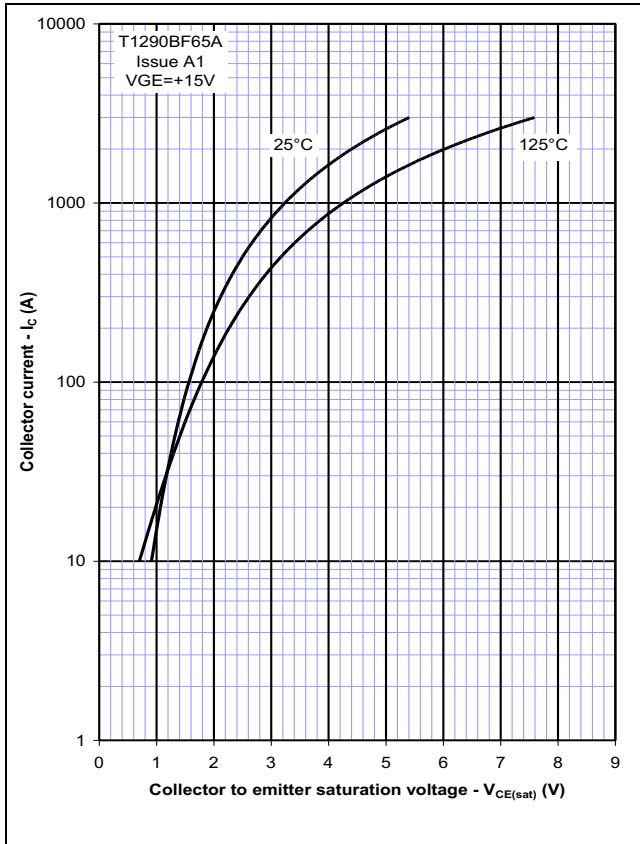


Figure 2 – Typical output characteristic

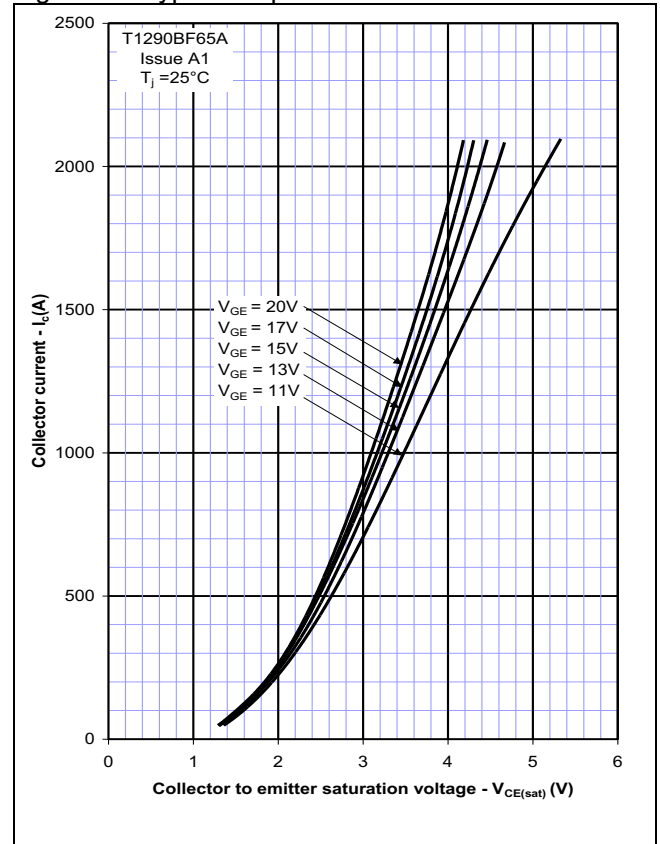


Figure 3 – Typical output characteristic

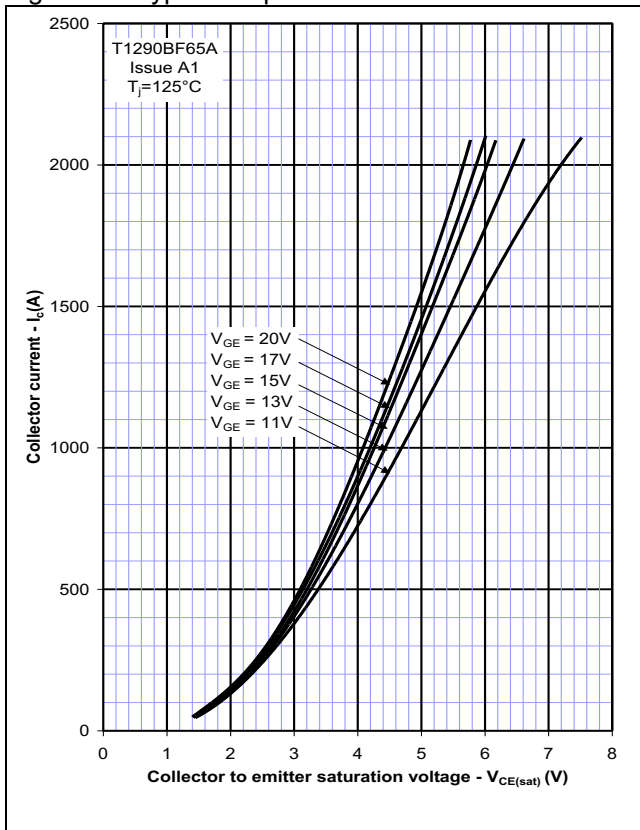


Figure 4 – Safe operating area (IGBT)

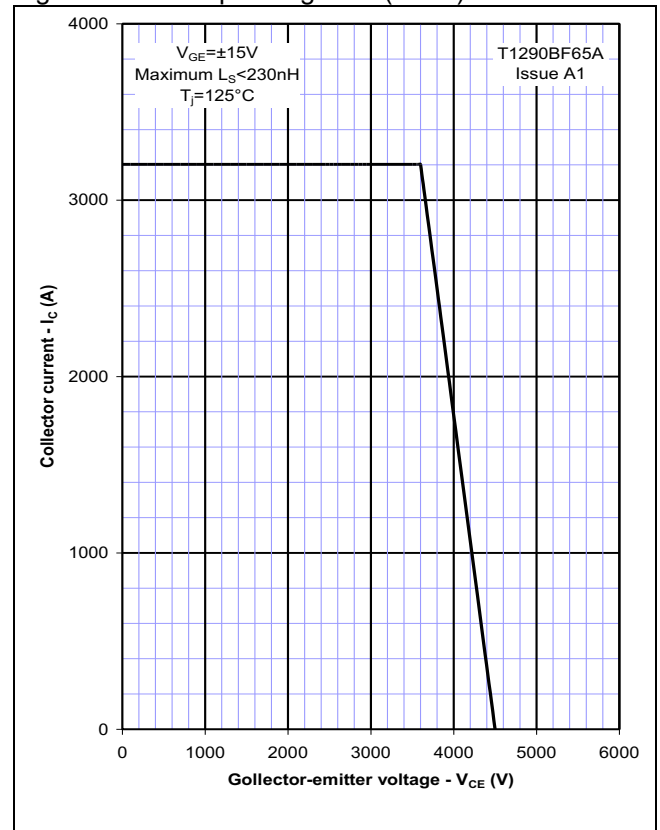


Figure 5 – Typical diode forward characteristics

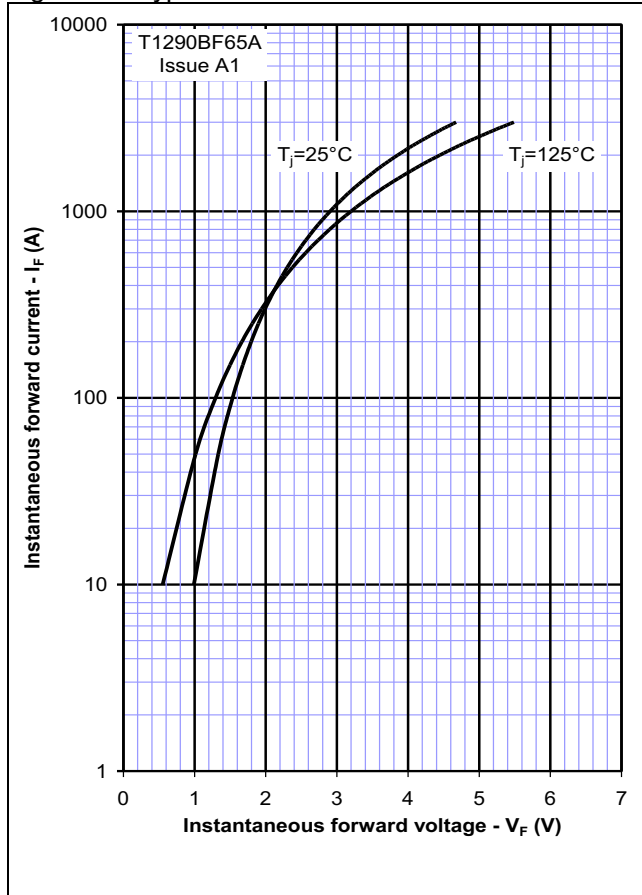


Figure 6 – Safe operating area (Diode)

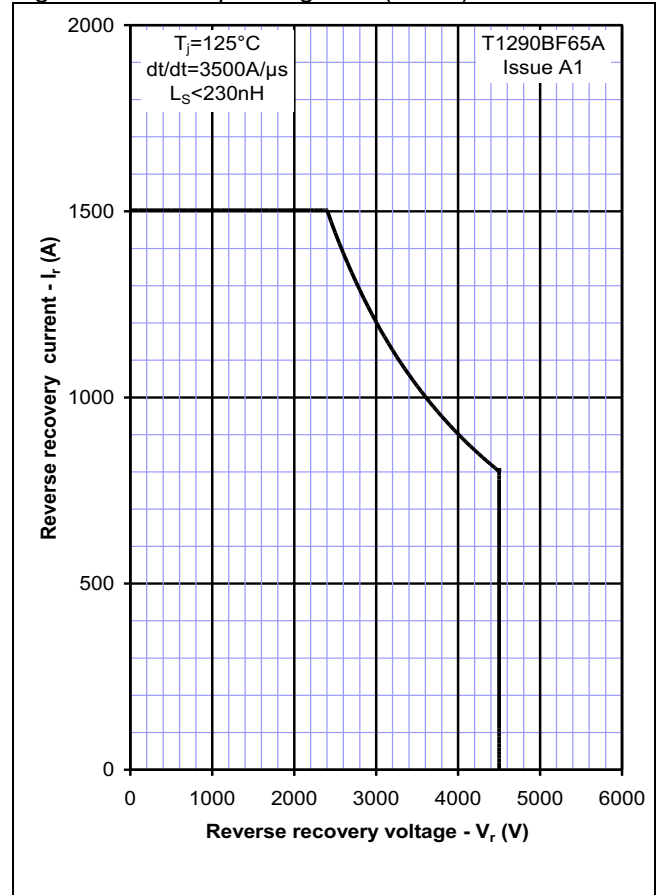


Figure 7 – Transient thermal impedance (IGBT)

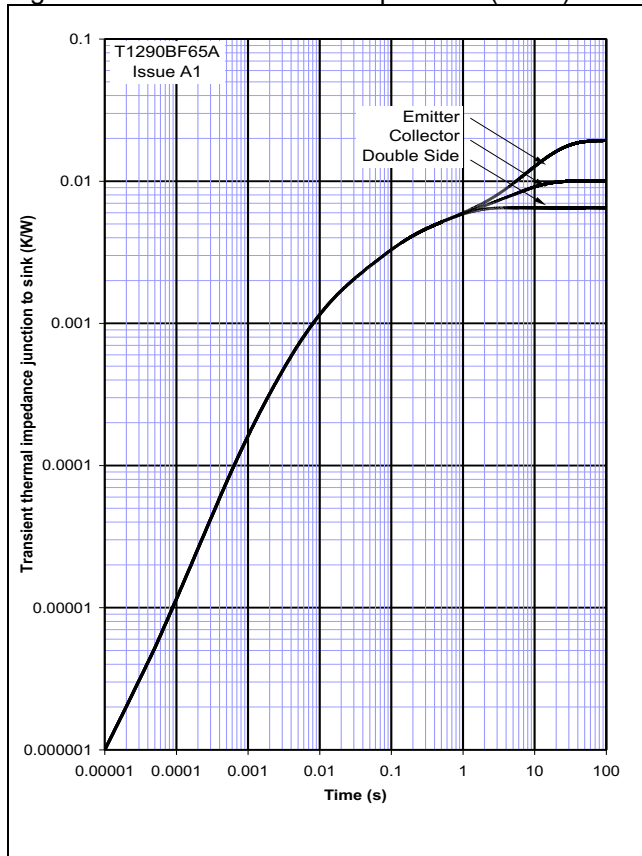
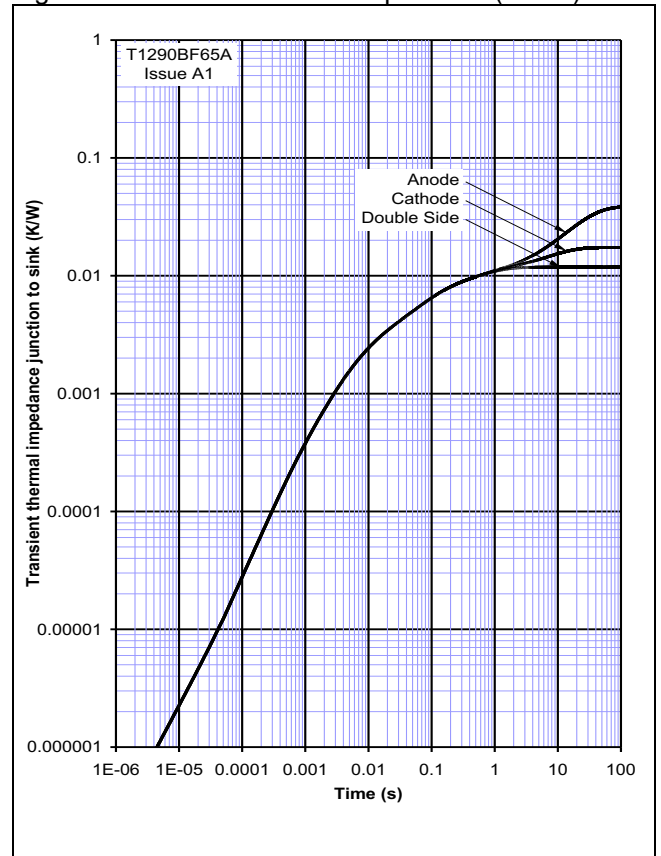
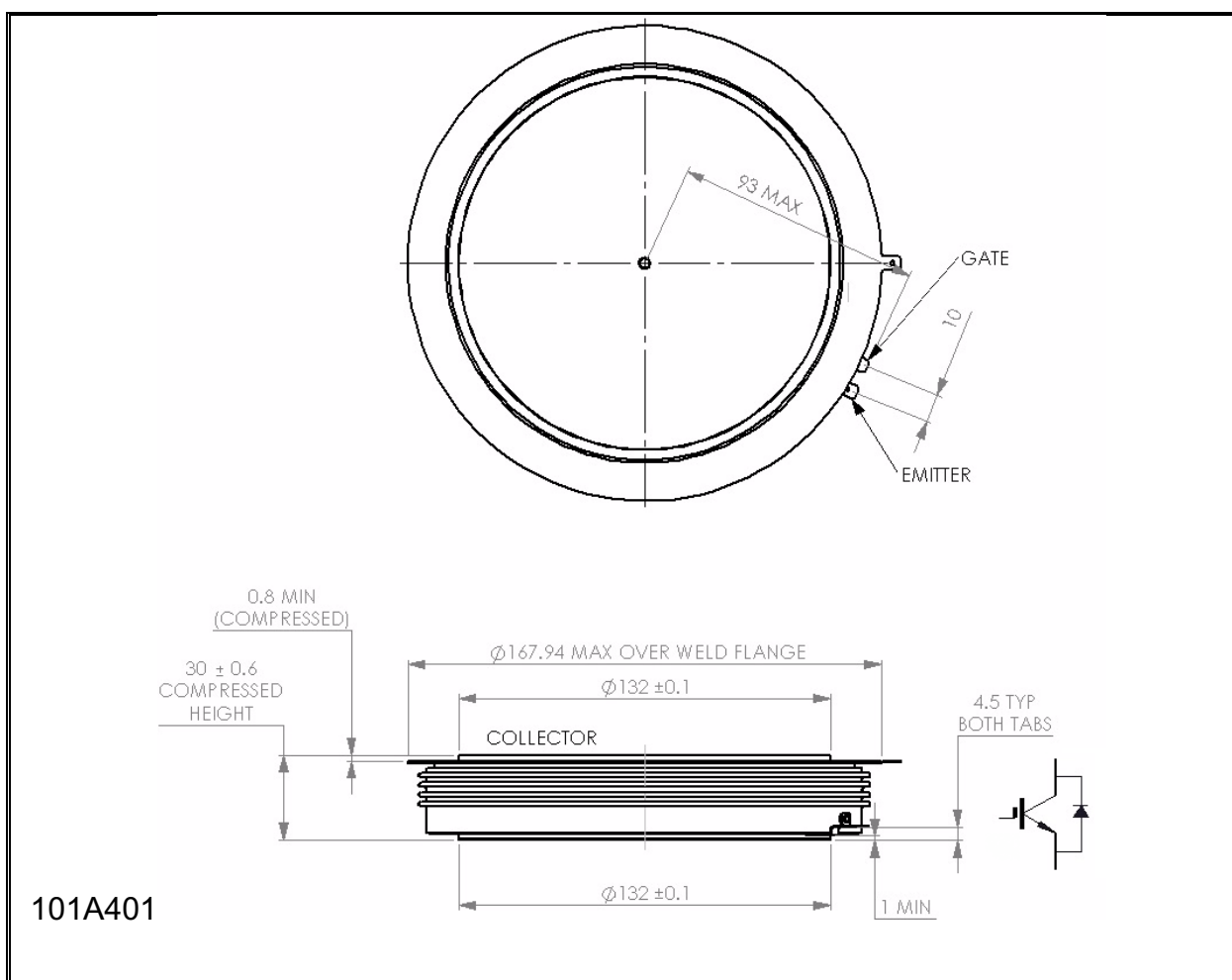


Figure 8 – Transient thermal impedance (Diode)



Outline Drawing & Ordering Information



ORDERING INFORMATION

(Please quote 10 digit code as below)

T1290	BF	65	A
Fixed type Code	Fixed Outline Code	Voltage Grade V _{CES} /100 65	Fixed format code

Typical order code: T1290BF65A ($V_{CES} = 6500V$)

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