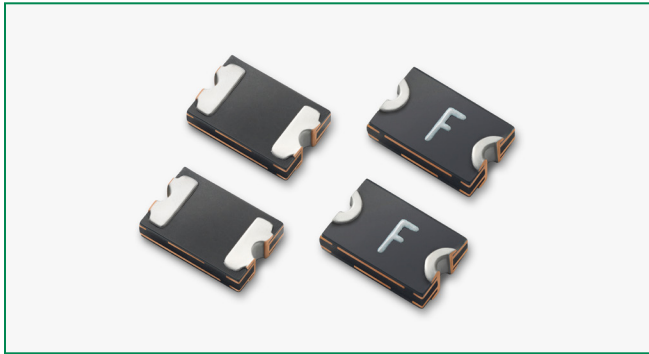


Automotive picoASMDC Series



Features

- Products meet applicable automotive industry standards
- Compatible with high-volume electronics assembly
- Small footprint – 0805 size
- Resettable solution against overcurrent and short-circuit
- AEC-Q200 qualified, RoHS compliant, and ISO/TS16949 certified
- Surface-mount form factor

Additional Information



Datasheet



Resources



Samples

Description

The picoASMDC is the first small-sized 0805 PPTC series from Littelfuse with AEC-Q200 qualification for automotive applications and it provides surface mount overcurrent protection for applications where space is a prime concern and resettable protection is desired.

Applications

- Automotive and industrial transportation
- Actuators and medium motors
- Trace protection
- Harness/junction box protection
- Powered outputs
- Electronic control modules
- Telematics/Infotainment

Benefits

- Expertise from the world's leading resettable overcurrent protection manufacturer
- Provides wider range of form factors to enhance design flexibility
- Meets ever-increasing demand for compact and space-saving designs due to more electronics content in vehicle
- Restores system operation after reset when fault condition is removed, thus provides safety and protection
- Able to meet most stringent requirements for the extreme harsh automotive environment
- Suitable for standard PCB assembly to enable automated mass production

Electrical Characteristics

Part Number	$I_H(A)@$	$I_T(A)@$	I_T	V_{MAX}	I_{MAX}	P_D Typ	Max Time-to-trip		R_{MIN}	R_{1MAX}	R_{aMAX}
	(R_{1MAX})	(R_{aMAX})					(A)	(V_{DC})			
picoASMDC – 15V											
picoASMDC010S	0.10	0.10	0.30	15	20	0.50	0.50	0.60	1.50	11.00	11.00
picoASMDC012S	0.12	0.12	0.30	15	20	0.50	1.00	0.10	1.50	9.00	9.00

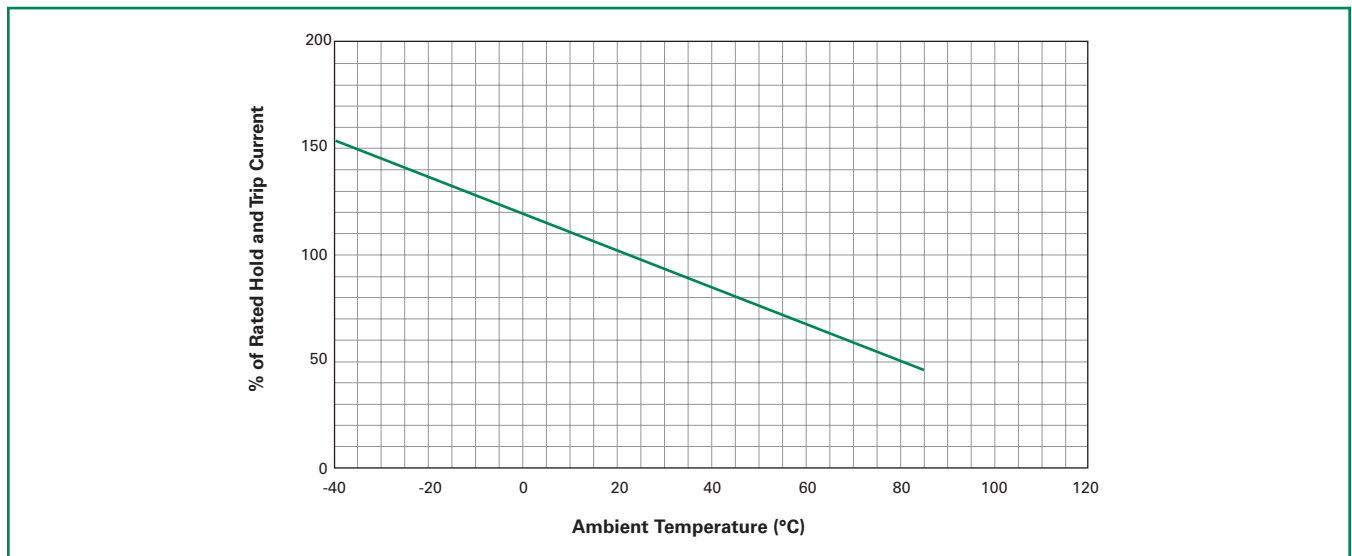
Notes:

- I_H : Hold current: maximum current device will pass without interruption in 25°C, unless otherwise specified
- I_T : Trip current: minimum current that will switch the device from low-resistance to high-resistance in 25°C still air, unless otherwise specified.
- V_{MAX} : Maximum voltage device can withstand without damage at rated current.
- I_{MAX} : Maximum fault current device can withstand without damage at rated voltage.
- P_D : Power dissipated from device when in the tripped state in 25°C still air, unless otherwise specified.
- R_{MIN} : Minimum resistance of device as supplied at 25°C, unless otherwise specified.
- R_{1MAX} : Maximum resistance of device when measured one hour post reflow, unless otherwise specified.
- R_{aMAX} : Maximum functional resistance of device after being subjected to the stresses described in PS400 at 25°C, unless otherwise specified.

Temperature Rerating

Maximum Ambient Temperature										
Part Number	-40°C	-20°C	0°C	20°C	25°C	40°C	50°C	60°C	70°C	85°C
Hold Current (A)										
picoASMDC - 15V										
picoASMDC010S	0.17	0.15	0.13	0.11	0.10	0.09	0.08	0.07	0.06	0.05
picoASMDC012S	0.20	0.17	0.15	0.13	0.12	0.10	0.09	0.08	0.07	0.05

Temperature Rerating Curve

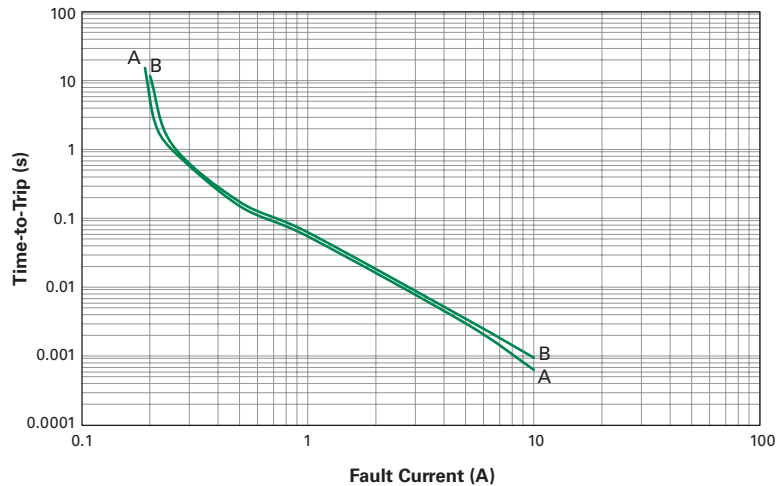


Typical Time-to-Trip Curves at 25°C

picoASMDC

A = picoASMDC010S

B = picoASMDC020S



Physical Specifications

Terminal Pad Material	100% Matte Tin with Nickel Underplate
Soldering Characteristics	Solderability per ANSI-J-STD-002 Category 3
Solder Heat Withstand	per IEC 60068-2-20, Test Tb, Section 5, Method 1a
Flammability	per IEC 60695-11-5 Needle Flame Test for 20 seconds
Recommended Storage Conditions	40°C max, 70% RH max; Devices May Not Meet Specified Ratings if Storage Conditions are Exceeded
Operation Temperature	-40°C~85°C

Note: See PS400 for other physical specifications.

Environmental Specifications

Test	Conditions	Resistance Change
Passive Aging	60°C, 1000 hrs	±3% Typical
	85°C, 1000 hrs	±5% Typical
Humidity Aging	85°C, 85% R.H., 100 hrs	±1.2% Typical
Thermal Shock	85°C, -40°C 20 times	-33% Typical
	125°C, -55°C 10 times	-33% Typical
Solvent Resistance	Freon	No change
	Trichloroethane	No change
	Hydrocarbons	No change

Note: See PS400 for other environmental specifications.

Moisture Resistance Level	Level 1, J-STD-020
Storage Conditions	40°C max, 70% RH max; devices should remain in original sealed bags prior to use. Devices may not meet specified values if these storage conditions are exceeded.

Dimension Figures

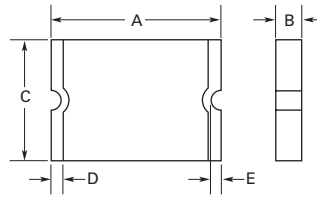
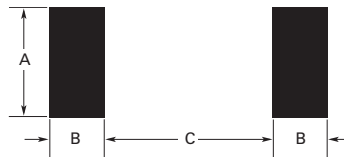


Figure 1

Dimensions

Part Number	Dimensions in Millimeters (Inches)										Figure
	A		B		C		D		E		
	Min	Max	Min	Max	Min	Max	Min	Max	Min	Max	
picoASMDC – 15V											
picoASMDC010S	2.00 (0.079)	2.20 (0.087)	0.60 (0.023)	1.00 (0.040)	1.30 (0.051)	1.50 (0.059)	0.25 (0.010)	0.75 (0.030)	0.076 (0.003)	—	1
picoASMDC012S	2.00 (0.079)	2.20 (0.087)	0.44 (0.017)	0.68 (0.027)	1.30 (0.051)	1.50 (0.059)	0.25 (0.010)	0.75 (0.030)	0.076 (0.003)	—	1

Recommended Pad Layout



Packaging and Marking Information

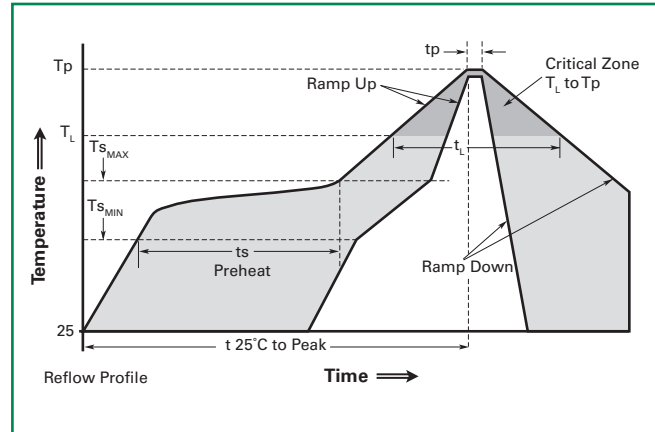
Part Number	Tape and Reel Quantity	Standard Package	Part Marking	Recommended Pad Layout Figures [mm (in)]		
				Dimension A (Min*/Nom)	Dimension B (Nom)	Dimension C (Nom)
picoASMDC – 15V						
picoASMDC010S	3,000	15,000	C	1.50 (0.060)	1.00 (0.039)	1.20 (0.047)
picoASMDC012S	4,000	20,000	F	1.50 (0.060)	1.00 (0.039)	1.20 (0.047)

* These devices are intended for use in automotive applications.

Solder Reflow Recommendations

Profile Feature	Pb-Free Assembly
Average ramp up rate (T_{s_MAX} to T_p)	3°C/s max
Preheat	
• Temperature min (T_{s_MIN})	150°C
• Temperature max (T_{s_MAX})	200°C
• Time (t_{s_MIN} to t_{s_MAX})	60-120 s
Time maintained above:	
• Temperature (T_L)	217°C
• Time (t_L)	60-150 s
Peak/Classification temperature (T_p)	260°C
Time within 5°C of actual peak temperature	
Time (t_p)	30 s max
Ramp down rate	3°C/s max
Time 25°C to peak temperature	8 min max

Note: All temperatures refer to topside of the package, measured on the package body surface.



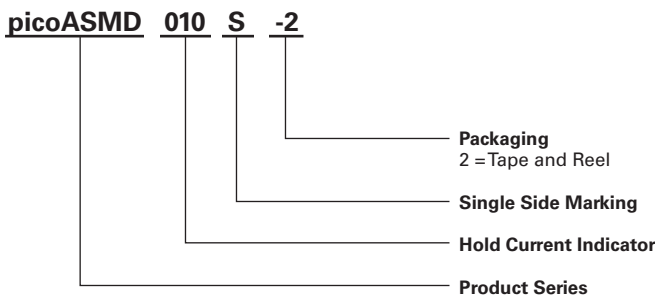
Solder Reflow

- Recommended reflow method: IR, hot air, nitrogen.
- Recommended maximum paste thickness: 0.25mm (0.010in)
- Devices can be cleaned using standard methods and aqueous solvents.
- Experience has shown the optimum conditions for forming acceptable solder fillets occur when a reasonable amount of solder paste is placed underneath each device's termination. As such, we request that customers comply with our recommended solder pad layouts.
- Customer should validate that the solder paste amount and reflow recommendations meet its application.
- We request that customer board layouts refrain from placing raised features (e.g. vias, nomenclature, traces, etc.) underneath PolySwitch devices. It is possible that raised features could negatively impact solderability performance of our devices.

Rework

- Standard industry practices. (Please also avoid direct contact to the device.)

Part Ordering Number System



Tape and Reel Specifications

Description	picoASMDC EIA 481-1	
	picoASMDC010S	picoASMDC012S
W	8.0 ± 0.30	8.0 ± 0.30
P₀	4.0 ± 0.10	4.0 ± 0.10
P₁	4.0 ± 0.10	4.0 ± 0.10
P₂	2.0 ± 0.10	2.0 ± 0.10
A₀	1.70 ± 0.1	1.70 ± 0.1
B₀	2.45 ± 0.1	2.45 ± 0.1
B₁ max	4.35	4.35
D₀	1.55 ± .05	1.55 ± .05
F	3.50 ± 0.05	3.50 ± 0.05
E₁	1.75 ± 0.10	1.75 ± 0.10
E₂ min	6.25	6.25
T max	0.3	0.3
T₁ max	0.1	0.1
K₀	1.12 ± 0.1	0.86 ± 0.1
A max	185	185
N min	50	50
W₁	8.4 + 1.5/-0.00	8.4 + 1.5/-0.00
W₂ max	14.4	14.4

Tape and Reel Diagrams

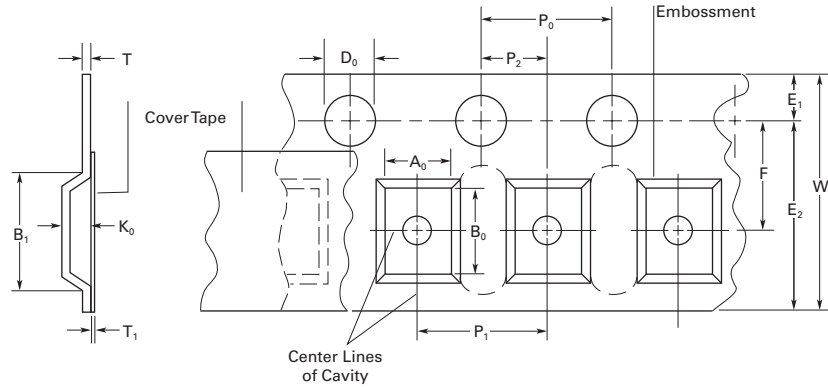


Figure 1

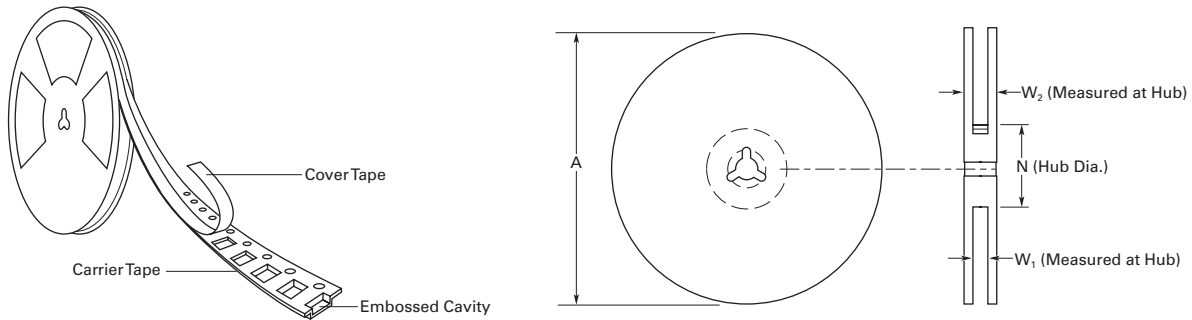


Figure 2

WARNING

- Users should independently evaluate the suitability of and test each product selected for their own application.
- Operation beyond the maximum ratings or improper use may result in device damage and possible electrical arcing and flame.
- These devices are intended for protection against damage caused by occasional overcurrent or overtemperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated.
- Contamination of the PPTC material with certain silicone-based oils or some aggressive solvents can adversely impact the performance of the devices.
- Device performance can be impacted negatively if devices are handled in a manner inconsistent with recommended electronic, thermal, and mechanical procedures for electronic components.
- PPTC devices are not recommended for installation in applications where the device is constrained such that its PTC properties are inhibited, for example in rigid potting materials or in rigid housings, which lack adequate clearance to accommodate device expansion.
- Operation in circuits with a large inductance can generate a circuit voltage (Ldi/dt) above the rated voltage of the device.

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